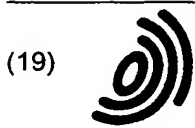


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Office européen des brevets



(11) **EP 1 189 274 A1**

(12) **EUROPEAN PATENT APPLICATION**
published in accordance with Art. 158(3) EPC

(43) Date of publication:
20.03.2002 Bulletin 2002/12

(21) Application number: 01902809.1

(22) Date of filing: 08.02.2001

(51) Int Cl.7: **H01L 21/68**, H01L 21/66,
H05B 3/10, H05B 3/16,
H05B 3/18, H05B 3/68,
C04B 35/00

(86) International application number:
PCT/JP01/00889

(87) International publication number:
WO 01/59833 (16.08.2001 Gazette 2001/33)

(84) Designated Contracting States:
**AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU
MC NL PT SE TR**

(30) Priority: 08.02.2000 JP 2000030896

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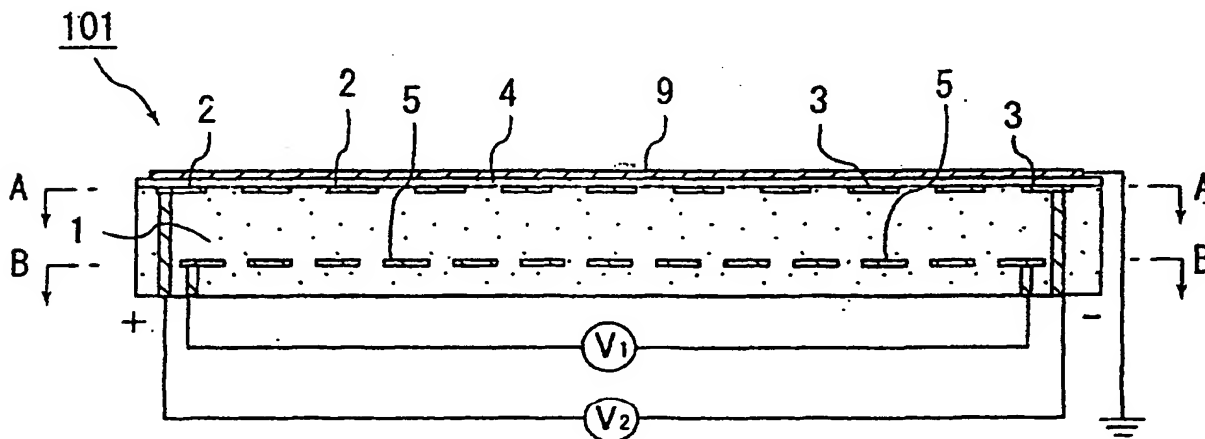
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(54) **CERAMIC BOARD FOR SEMICONDUCTOR PRODUCTION AND INSPECTION DEVICES**

(57) The object of the present invention is to provide a ceramic substrate that can provide a substantially uniform temperature distribution to a surface of the ceramic substrate where a semiconductor wafer is treated. A ceramic substrate for a semiconductor-producing/examin-

ing device according to the present invention is a ceramic substrate having a conductor formed on a surface of the ceramic substrate or inside the ceramic substrate, wherein said substrate is containing oxygen and having a disc form, the diameter thereof exceeding 250 mm and a thickness thereof being 25 mm or less.

Fig. 1



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Description

Technical Field

5 [0001] The present invention relates mainly to ceramic substrates used as a device for producing or examining semiconductors, such as ceramic substrates for hot plates (ceramic heaters), electrostatic chucks and wafer probers.

Background Art

10 [0002] As the device for producing or examining semiconductors including an etching device, a chemical vapor deposition device and the like, heaters or wafer probers and the like using substrates made of metal such as stainless steel or aluminum alloy, have been conventionally used.

[0003] However, heaters made of metal have problems that they have poor temperature control characteristics and they are heavy and bulky since they are thick; and also have another problem that they have poor corrosion resistance to corrosive gases.

15 [0004] In order to solve such problems, heaters which use ceramic such as aluminum nitride have been developed in place of heaters made of metal.

[0005] Since such ceramic heaters have the advantage that a warp and the like of ceramic substrates can be prevented even if they are not made so thick due to high rigidity thereof, they are especially suitable for wafer probers or electrostatic chucks.

20 [0006] In recent years, as the semiconductor wafer becomes larger and the diameter thereof becomes larger, the diameter of ceramic substrates used in electrostatic chucks and the like used for producing or examining of semiconductor wafers are required to be bigger so as to put the enlarged semiconductor wafers thereon. Thus, for example, a hot plate having a diameter of 300mm and a thickness of 17mm composed of aluminum nitride ceramic is disclosed in JP Kokai Hei 11-74064 and so on.

Summary of the Invention

30 [0007] There existed a problem that such enlargement of an electrostatic chuck and the like results in the deterioration of a temperature-rising property or a heat uniformity of a ceramic substrate, those are the properties being demanded at the production of semiconductor devices and the like.

[0008] The inventors of the present invention studied earnestly to solve the above problems. Consequently, they have found that the reason why the temperature-rising property or the heat uniformity of such a large ceramic substrate having a diameter exceeding 250mm is deteriorated is the increase of a heat capacity of a ceramic substrate. Simultaneously, they have found that the temperature-rising property and the heat uniformity can be improved: by lowering a barrier of heat conduction between ceramic particles through the improvement of sinterability by incorporating oxygen in a ceramic substrate; and by the reduction of the heat capacity itself through the adjustment of the thickness of the ceramic substrate to 25 mm or less.

40 [0009] Moreover, in the case of a large ceramic substrate which has a diameter exceeding 250 mm, if the thickness thereof is adjusted to 25 mm or less, a warp tends to be generated at a high temperature. They have found that the warp can be prevented almost completely by: forming a conductor at the position of 60% in a thickness-direction from the bottom face opposite to a wafer treating face of the ceramic substrate; and using a nitride ceramic containing oxygen or an oxide ceramic as the ceramic. Thus, they have accomplished the present invention.

45 [0010] In case a disk-shaped ceramic substrate has a diameter of 250 mm or more, the ceramic substrate is adjusted to have a thickness of 25 mm or less in order to reduce the heat capacity of the ceramic substrate. In such a case, if a resistance heating element is disposed inside the ceramic substrate, the distance between the resistance heating element and the heating face becomes shorter correspondingly, resulting in an occurrence of a temperature distribution similar to the pattern of the resistance heating element. The present invention has been accomplished through the finding that disposing of a conductor, such as a resistance heating element, on the back side of a ceramic substrate makes it possible to keep the substantial distance between the heating face and the resistance heating element, and can improve the temperature-rising property and the heat uniformity.

[0011] Besides, although the adjustment of the thickness of a ceramic substrate to 25 mm or less makes the ceramic substrate apt to warp at a high temperature, the present invention can also prevent such a warp.

55 [0012] In JP Kokai Hei 11-74064, a hot plate composed of aluminum nitride ceramic having a diameter of 300 mm and a thickness of 17 mm is disclosed, but there is no description or suggestion on the problem on the warp at a high temperature. The publication, therefore, never affect the novelty or inventive step of the present invention.

[0013] A first aspect of the present invention is a ceramic substrate for a semiconductor-producing/examining device having a conductor formed on a surface of the ceramic substrate or inside the ceramic substrate, wherein said substrate

is containing oxygen and has a disc form,

the diameter thereof exceeding 250 mm and a thickness thereof being 25 mm or less.

[0014] A second aspect of the present invention is a ceramic substrate for a semiconductor-producing/examining device having a conductor formed on a surface of the ceramic substrate, wherein said substrate has a disc form,

the diameter thereof exceeding 250 mm and a thickness thereof being 25 mm or less.

[0015] The above-mentioned first and second aspects of inventions are different in the position where a conductor is formed and in the respect whether oxygen is contained or not, but they are the same with respect to the other constituent features. The contents of the two inventions, therefore, will be described together below.

Brief Description of Drawings

[0016]

Fig. 1 is a longitudinal sectional view schematically showing an electrostatic chuck that is an embodiment of a ceramic substrate for use in a device for producing or examining a semiconductor according to the present invention.

Fig. 2 is an A-A line sectional view of the electrostatic chuck shown in Fig. 1.

Fig. 3 is a B-B line sectional view of the electrostatic chuck shown in Fig. 1.

Fig. 4 is a sectional view schematically showing an example of an electrostatic electrode of an electrostatic chuck.

Fig. 5 is a sectional view schematically showing an example of an electrostatic electrode of an electrostatic chuck.

Fig. 6 is a sectional view schematically showing a wafer prober that is an embodiment of a ceramic substrate of the present invention.

Fig. 7 is an A-A line sectional view in the wafer prober shown in Fig. 6.

In Fig. 8, (a) to (d) are sectional views schematically showing a part of the manufacture process of an electrostatic chuck.

Fig. 9 is a sectional view schematically showing a hot plate that is an embodiment of a ceramic substrate of the present invention.

Explanation of symbols

[0017]

101	Electrostatic chuck
1, 43	Ceramic substrate
2, 22, 32a, 32b	Chuck positive electrostatic layer
3, 23, 33a, 33b	Chuck negative electrostatic layer
2a, 3a	Semicircle arc part
2b, 3b	Comb teeth-shaped part
4	Ceramic dielectric film
5, 49	Resistance heating element
6, 18	External terminal pin
9	Silicon wafer
11	Bottomed hole
12	Through hole
16	Conductor-filled through hole
42	Chuck top conductor layer
45	Guard electrode
46	Ground electrode
47	Groove
48	Suction hole

Detailed Description of the Invention

[0018] The ceramic substrate for a semiconductor producing/examining device according to the present invention is a ceramic substrate having a conductor formed on a surface of the ceramic substrate or inside the ceramic substrate, wherein said substrate is containing oxygen and has a disc form, the diameter thereof exceeding 250 mm and a thickness thereof being 25 mm or less. It is desirable that the above-mentioned ceramic substrate contains oxygen or is an oxide ceramic.

[0019] In the ceramic substrate of the present invention, the above-mentioned ceramic substrate is made large to have a diameter thereof exceeding 250 mm, and at the same time, the thickness thereof being 25mm or less, so that the increase in the weight of the whole ceramic substrate is suppressed. Thus, the heat capacity of the ceramic substrate can be prevented from becoming too large, resulting in a ceramic substrate having uniform enough temperature distribution that raises no problems in the processing of semiconductor wafers and the like.

[0020] If the thickness of a ceramic substrate exceeds 25 mm, the heat capacity of the ceramic substrate becomes large. Particularly, if the ceramic substrate is heated and cooled by using temperature control means, the large heat capacity causes the deterioration in temperature-following property.

[0021] The thickness of the ceramic substrate is desirably 10 mm or less, and more desirably 5 mm or less. If it exceeds 10 mm, the heat capacity of the ceramic substrate at a temperature of 200 °C or more will become large, and the temperature controllability thereof, and the temperature uniformity of the face on which a semiconductor wafer will be placed, tend to become poor.

[0022] The above-mentioned ceramic substrate is used in a temperature range of 100 to 700 °C. This is because in a temperature range of 100 °C or more, the Young's modulus of the ceramic is lowered and the ceramic tends to warp, so the advantageous effect of the present invention is beneficial.

[0023] The above-mentioned ceramic substrate desirably has a plurality of through holes in which lifter pins for a semiconductor wafer will be inserted. The reason is that when a ceramic substrate has through holes, the Young's modulus is lowered at a temperature of 100 °C or more, resulting in the release of the stress generated during processing, thus the substrate tends to warp. The advantageous effect of the present invention, therefore, become significant. The diameter of the through holes is desirably 0.5 mm to 30 mm.

[0024] In the ceramic substrate of the present invention, a silicon wafer is put on a surface of the ceramic substrate in the state that they contact each other. Besides, the silicon wafer may be supported by lifter pins and the like and held in the state that a given interval is kept between the silicon wafer and the ceramic substrate (reference to Fig. 9). Such a face on which a semiconductor wafer is placed or held will, in the following description, be expressed as a wafer treating face. When a semiconductor wafer is supported with a supporting pin, for example, a concave portion is formed in a ceramic substrate and a supporting pin is fitted in a concave portion so that its tip slightly projects from the wafer treating face and a semiconductor wafer is supported by the supporting pin.

[0025] When heating is conducted while a distance between a ceramic substrate and a semiconductor wafer is kept constant, the distance is desirably 50 to 5000 μm .

[0026] In the case that heating is conducted while a distance between a ceramic substrate and a semiconductor wafer is kept constant, unless the distance between the semiconductor wafer and the holding face of the ceramic substrate is made to be constant, the semiconductor wafer cannot be heated uniformly. For this reason, there is a necessity of minimizing the warp amount of a ceramic substrate thus, the present invention can act particularly advantageously.

[0027] In the present invention, the warp amount is desirably less than 70 μm in a temperature range of 100 to 700 °C. This is because if it exceeds 70 μm , a distance between a treating face (the heating face) of a ceramic substrate and a semiconductor wafer becomes uneven and the semiconductor wafer cannot be heated uniformly.

[0028] The reason why the diameter of the ceramic substrate is set to be larger than 250 mm is that a diameter of a semiconductor wafer is becoming mainly 10 inches or more, and there is also a need for the ceramic substrates to become larger. Thus, the above-mentioned ceramic substrate is preferably 12 inches (300 mm) or more. This is because such a ceramic substrate will become the main stream of semiconductor wafers of the next generation. In addition, ceramic substrates having a diameter larger than 250 mm tend to warp due to their own weight and the like at a high temperature. Such a warp remarkably occurs in a ceramic with a thickness of 25 mm or less. In the present invention, by providing a conductor in a specific region of a ceramic wherein the warp easily occurs at such a high temperature, such a warp can be prevented.

[0029] The above-mentioned conductor is desirably disposed: in a region up to the position of 60% in the thickness-direction from the face opposite to the wafer treating face of the ceramic substrate; or on the above-mentioned face opposite to the wafer treating face. The warp is caused by the own weight or, in the case of ceramic substrates for wafer probers (hereinafter, mentioned as wafer probers), is caused by the pressure of probing. Therefore, when a ceramic substrate warps, a pulling force is applied on the face opposite to the wafer treating face. In the present invention, a ceramic substrate becomes able to resist to such a pulling force by providing it with a conductor and, thus the warp can be prevented.

[0030] Examples of the above-mentioned conductor include a conductive ceramic, a metal foil, a metal sintered body, a metal wire and the like. Generally, even at a high temperature, a Young's modulus of a metal does not drop easily. Hence, even if the Young's modulus of a ceramic drops at a high temperature, the presence of a metal foil, metal sintered body, metal wire and the like can prevent the drop in a Young's modulus of the whole. Moreover, since the above-mentioned conductive ceramic also has a bonding structure or a crystal structure similar to those of metal due to the fact that the conductive ceramic is conductive, its Young's modulus is not easily lowered at a high temperature

and a ceramic substrate, therefore, can be prevented from warping at a high temperature.

[0031] Also, in the case in which the above-mentioned conductor serves as a resistance heating element, it is desirably disposed: in a region up to the position of 50% in the thickness-direction from the face opposite to the wafer treating face of the ceramic substrate; or on the above-mentioned face opposite to the wafer treating face of the ceramic substrate. This is because: when heat is transmitted from a heating element to a wafer treating face through the inside of a ceramic substrate, the heat diffuses in the ceramic substrate to produce uniform heat, thus as a distance between the wafer treating face and the heating element becomes larger, the surface temperature of the wafer treating face tends to be uniform.

[0032] In the ceramic substrate for use in a semiconductor device according to the present invention, a ceramic substrate having a Young's modulus of 280 GPa or more in a temperature range of 25 to 800 °C is desirably employed.

[0033] This is because if a Young's modulus is less than 280 GPa, the rigidity is too low and, therefore, it is difficult to reduce the warp amount during the heating and then, the warp may cause the damage of a semiconductor wafer.

[0034] In the present invention, it is desirable that there are no pores, or even if there are pores, the pore diameter of the largest pore is 50 μm or less.

[0035] When there are no pores, a breakdown voltage at a high temperature becomes particularly high. On the other hand, when there are pores, a fracture toughness value becomes high. Accordingly, which design to take depends upon the required characteristics.

[0036] The reason why a high fracture toughness value is caused by the presence of pores is not clear, but it is presumed that the development of cracks is stopped by the pores.

[0037] In the present invention, the largest pore desirably has a pore diameter of 50 μm or less because if it exceeds 50 μm , a high breakdown voltage characteristic cannot be kept particularly at a temperature of 200 °C or more.

[0038] The pore diameter of the largest pore is more desirably 10 μm or less because a warp amount decreases at a temperature of 200 °C or more.

[0039] The porosity and the pore diameter of the largest pore are adjusted: by a length of pressing time, pressure and temperature during sintering; and by an additive such as SiC or BN. SiC or BN can introduce pores since they inhibit sintering.

[0040] At the measurement of the pore diameter of the largest pore, 5 samples are prepared. Then, after polishing their surfaces and photographing the surfaces of the samples at 10 points for every samples with an electron microscope at a magnification of 2000 to 5000 times, the largest pore diameter is chosen in every photograph taken and the average of the 50 shots is defined as the largest pore diameter.

[0041] The above-mentioned nitride ceramic substrate desirably contains 0.05 to 10% by weight of oxygen. This is because, when the oxygen content is less than 0.05% by weight, a high breakdown voltage cannot be kept and a warp at a high temperature cannot be prevented, and, on the other hand, when the content exceeds 10% by weight, a breakdown voltage drops due to the drop of the high breakdown voltage characteristic of the oxide at a high temperature. This is also because, when the oxygen amount thereof exceeds 10% by weight, temperature rising/falling property becomes poor due to the drop in heat conductivity. Particularly, a content of 0.1 to 5% by weight is optimum.

[0042] Oxygen can be introduced: by heating raw material in the air or in oxygen; or by adding a sintering aid.

[0043] A ceramic containing oxygen (a nitride ceramic containing oxygen, a carbide ceramic containing oxygen and an oxide ceramic) do not easily warp at a high temperature because their Young's modulus do not easily drop at a high temperature.

[0044] The porosity is measured by an Archimedes' method. A sintered body is pulverized, and then the pulverized pieces are put in an organic solvent or mercury to determine its volume.

[0045] Then the true specific gravity of the pieces is obtained from the weight and the measured volume thereof, and the porosity is calculated from the true specific gravity and apparent specific gravity.

[0046] The ceramic material constituting the ceramic substrate of the present invention is not particularly limited and is exemplified by a nitride ceramic, a carbide ceramic and an oxide ceramic and the like.

[0047] Examples of the above-mentioned nitride ceramic include a metal nitride ceramic such as aluminum nitride, silicon nitride, boron nitride and titanium nitride and the like.

[0048] Examples of the above-mentioned carbide ceramic include a metal carbide ceramic such as silicon carbide, zirconium carbide, titanium carbide, tantalum carbide and tungsten carbide and the like.

[0049] Examples of the above-mentioned oxide ceramic include a metal oxide ceramic such as alumina, zirconia, cordierite and mullite and the like.

[0050] These ceramics may be used either alone or in combination of two or more kinds of them.

[0051] Among these ceramics, a nitride ceramic and an oxide ceramic are preferable because they do not easily cause a warp at a high temperature.

[0052] Among a nitride ceramic, aluminum nitride is most desirable because it has the highest heat conductivity as high as 180 W/m·K.

[0053] In the present invention, the ceramic substrate desirably contains a sintering aid.

[0054] As the sintering aid, an alkali metal oxide, an alkaline earth metal oxide and a rare-earth oxide can be used. Among these sintering aids, CaO , Y_2O_3 , Na_2O , Li_2O and Rb_2O_3 are desirable particularly. Alumina may also be used. The content thereof is desirably 0.1 to 20% by weight.

[0055] In the present invention, the ceramic substrate desirably contains 50 to 5000 ppm of carbon.

[0056] This is because, by incorporating carbon into the substrate, the ceramic substrate is made to be black, thus sufficient use of radiation heat is possible in the case of using the ceramic substrate as a heater.

[0057] The carbon may be either amorphous one or crystalline one. This is because the use of amorphous carbon can prevent a drop

in a volume resistivity at a high temperature and the use of crystalline carbon can prevent a drop in a heat conductivity at a high temperature. Accordingly, depending on the purpose, both crystalline carbon and amorphous carbon may be used together. The content of, carbon is desirably 200 to 2000 ppm.

[0058] When the ceramic substrate contains carbon, the ceramic substrate desirably contains carbon so as to have a brightness of N6 or less based on the provision given in JIS Z 8721. This is because a ceramic substrate having such a brightness is superior in radiant heat capacity and covering-up property.

[0059] Herein, the brightness N is defined as follows: the brightness of ideal black is made to 0; that of ideal white is made to 10; respective colors are divided into 10 parts in the manner that the brightness of the respective colors is recognized stepwise between the brightness of black and that of white at equal intensity intervals; and the resultant parts are indicated by symbols N0 to N10, respectively.

[0060] Actual brightness is measured by comparison with color chips corresponding to N0 to N10. One place of decimals in this case is made to 0 or 5.

[0061] The ceramic substrate of the present invention is a ceramic substrate used for a device for producing semiconductors or examining semiconductors. Examples of the concrete device include an electrostatic chuck, a wafer prober, a hot plate and a susceptor and the like.

[0062] Fig. 1 is a longitudinal sectional view schematically showing an example of an electrostatic chuck, which is an embodiment of a ceramic substrate of the present invention. Fig. 2 is a sectional view taken along the A-A line in the electrostatic chuck illustrated in Fig. 1. Fig. 3 is a sectional view taken along the B-B line in the electrostatic chuck illustrated in Fig. 1.

[0063] In this electrostatic chuck 101, an electrostatic electrode layer comprising a chuck positive electrostatic layer 2 and a chuck negative electrostatic layer 3 is embedded in a ceramic substrate 1 in a circular shape as viewed from the above. A silicon wafer 9 is placed on the electrostatic chuck 101, which is grounded.

[0064] Since a ceramic layer which is formed on this electrostatic electrode layer so as to cover the electrostatic electrode layer, functions as a dielectric film for adsorbing a silicon wafer, it will be referred to a ceramic dielectric film 4, hereinafter.

[0065] As illustrated in Fig. 2, the chuck positive electrostatic layer 2 comprises a semicircular arc part 2a and a comb teeth-shaped part 2b and the chuck negative electrostatic layer 3 similarly comprises a semicircular arc part 3a and a comb teeth-shaped part 3b. These chuck positive electrostatic layer 2 and chuck negative electrostatic layer 3 are arranged opposite to each other so that the comb-teeth-shaped parts 2b and 3b cross each other. The + side and the - side of a direct power source are connected to the chuck positive electrostatic layer 2 and chuck negative electrostatic layer 3, respectively. Thus, a direct current V_2 is applied thereto.

[0066] In order to control the temperature of a silicon wafer 9, resistance heating elements 5 in a concentric configuration as viewed from the above are disposed inside the ceramic substrate 1, and external terminal pins 6 are connected and fixed to both ends of each of the resistance heating elements 5 so that a voltage V_1 can be applied. Although not shown in Figs. 1,2, in the ceramic substrate 1, bottomed holes 11 in which temperature measuring devices are inserted; and through holes 12 through which lifter pins (not shown) for supporting and moving up and down a silicon wafer 9 are inserted, are formed. The resistance heating elements 5 may also be disposed on the bottom face of the ceramic substrate. The ceramic substrate 1 may have an RF electrode embedded therein based on the necessity.

[0067] When the electrostatic chuck 101 is caused to function, a direct voltage V_2 is applied between the chuck positive electrostatic layer 2 and the chuck negative electrostatic layer 3. Thus, a silicon wafer 9 is adsorbed to be fixed to these electrodes through a ceramic dielectric film 4 by the electrostatic action of the chuck positive electrostatic layer 2 and the chuck negative electrostatic layer 3. After fixing the silicon wafer 9 to an electrostatic chuck 101 in this manner, the silicon wafer 9 is subjected to various treatments such as CVD and the like.

[0068] The above electrostatic chuck is equipped with electrostatic electrode layers and resistance heating elements and has a structure, for example, shown in Figs. 1 to 3. Hereinafter, among the parts constituting the above-mentioned electrostatic chuck, parts which have not been described in the description on the above-mentioned ceramic substrate will be explained.

[0069] A ceramic dielectric film 4 on the electrostatic electrodes is desirably made of the same material as the other parts of the ceramic substrate because a green sheet and the like can be manufactured in the same process and a ceramic substrate can be manufactured via a single sintering operation after laminating those sheets.

[0070] It is desirable that the above-mentioned ceramic dielectric film, like the other portions of the ceramic substrate, contains carbon since an electrostatic electrode can be hidden and radiation heat can be utilized.

[0071] The above-mentioned ceramic dielectric film desirably contains an alkali metal oxide, an alkaline earth metal oxide or a rare-earth oxide because these serve as a sintering aid and the like, resulting in formation of a high-density dielectric film.

[0072] The thickness of the above-mentioned ceramic dielectric film is desirably 50 to 5000 μm . This is because of the following reasons: if the ceramic dielectric film has a thickness less than 50 μm , the film is too thin to obtain a sufficient breakdown voltage and the ceramic dielectric film may have a dielectric breakdown when a silicon wafer is placed and adsorbed; and if the thickness of the above-mentioned ceramic dielectric film exceeds 5000 μm , a long distance between a silicon wafer and an electrostatic electrode lowers the ability of adsorbing the silicon wafer. The thickness of the ceramic dielectric film is more desirably 100 to 1500 μm .

[0073] Examples of the electrostatic electrode formed inside the ceramic substrate include sintered bodies of metal or conductive ceramic and metal foils. Desirable metal sintered body is those made of at least one selected from tungsten and molybdenum. The metal foils are also desirable to be made of the same material as the metal sintered body. This is because these metals are hard to be oxidized and have sufficient conductivities as an electrode. As a conductive ceramic, at least one selected from carbides of: tungsten; and molybdenum can be employed.

[0074] Figs. 4, 5 are horizontal sectional views schematically illustrating electrostatic electrodes in other electrostatic chucks. In the electrostatic chuck 20 shown in Fig. 4, a chuck positive electrostatic layer 22 and a chuck negative electrostatic layer 23, both being in a semicircular shape, are formed inside the ceramic substrate 1. In the electrostatic chuck illustrated in Fig. 5, are arranged chuck positive electrostatic layers 32a, 32b and chuck negative electrostatic layers 33a, 33b, both types of layers having a shape obtained by dividing a circular shape in four. Two positive electrostatic layers 22a, 22b and two negative electrostatic layers 33a and 33b are formed so as to cross each other.

[0075] When forming electrodes in a shape obtained by dividing an electrode having a circular shape and the like, the number of divided pieces is not particularly limited and may be 5 or more. The shape of the electrode is also not limited to a sector form.

[0076] A resistance heating element may be formed inside a ceramic substrate as illustrated in Fig. 1, and may also be disposed on the bottom face of the ceramic substrate. When a resistance heating element is disposed, a supporting case into which an electrostatic chuck is fitted may be provided with an inlet for blowing a coolant, such as the air, as cooling means.

[0077] Examples of resistance heating elements include sintered body made of metal or conductive ceramic, metal foils and metal wires and the like. As the metal sintered body, at least one selected from tungsten and molybdenum is desirable because these metals are relatively hard to be oxidized and have resistivities sufficient to generate heat.

[0078] As the conductive ceramic, at least one selected from carbides of: tungsten; and molybdenum can be employed.

[0079] Furthermore, when a resistance heating element is formed on the bottom face of a ceramic substrate, it is desirable to use noble metal (gold, silver, palladium, platinum) or nickel as the sintered body. Specifically, silver, silver-palladium and the like can be used.

[0080] As metal particles to be used for the above-mentioned metal sintered body, those of a spherical shape, those of a scaly shape, and mixtures of those of a spherical shape and those of a scaly shape can be used.

[0081] A metal oxide may be added in the metal sintered body. The purpose of using the above-mentioned metal oxide is to let a ceramic substrate adhere to metal particles firmly. The reason of the improvement in the adhesion between a ceramic substrate and metal particles is not clear, but would be as follows: an oxide film is slightly formed on the surface of the metal particles and an oxide film is formed on the surface of the ceramic substrate in the case that the ceramic substrate is made of a non-oxide ceramic as well as an oxide ceramic. It can be therefore considered that these oxide films are sintered and integrated with each other, through the metal oxide, on the surface of the ceramic substrate so that the metal particles and the ceramic substrate adhere closely to each other.

[0082] As the above metal oxide, for example, at least one selected from lead oxide, zinc oxide, silica, boron oxide (B_2O_3), alumina, yttria and titania is desirable because these oxides can improve the adhesiveness between metal particles and a ceramic substrate without increasing the resistivity of a resistance heating element.

[0083] The amount of the metal oxide is desirably 0.1 part by weight or more and less than 10 parts by weight per 100 parts by weight of metal particles because the use of a metal oxide in this range can improve the adhesiveness between metal particles and a ceramic substrate without too much increase of resistivity.

[0084] The rates of lead oxide, zinc oxide, silica, boron oxide (B_2O_3), alumina, yttria and titania, based on 100 parts by weight of the whole amount of the metal oxides, are preferably 1 to 10 parts by weight for lead oxide, 1 to 30 parts by weight for silica, 5 to 50 parts by weight for boron oxide, 20 to 70 parts by weight for zinc oxide, 1 to 10 parts by weight for alumina, 1 to 50 parts by weight for yttria and 1 to 50 parts by weight for titania. The ratio is preferably adjusted within the scope that the total amount of these oxides is not over 100 parts by weight. This is because these ranges are particularly those where the adhesion to a ceramic substrate can be improved.

[0085] When a resistance heating element is disposed on the bottom face of a ceramic substrate, it is desirable that a surface of the resistance heating element is covered with a metal layer. The resistance heating element is a sintered body of metal particles. Hence, if it is exposed, it is oxidized easily and the oxidization thereof results in a change in the resistivity. Then, such oxidation can be prevented by covering the surface with a metal layer.

[0086] A desirable thickness of the metal layer is 0.1 to 10 μm because this is the range where the oxidation of a resistance heating element can be prevented without a change in the resistivity of the resistance heating element.

[0087] The metal used for the covering may be any non-oxidizable metal. Specifically, at least one kind of metal selected from gold, silver, palladium, platinum and nickel is desirable. Among them, nickel is more desirable because a resistance heating element is required to have a terminal for the connection to a power supply, the terminal being attached to the resistance heating element through a solder, and nickel can prevent thermal diffusion of the solder. As a connection terminal, a terminal pin made of Kovar can be employed.

[0088] It is to be noted that when a resistance heating element is formed inside a heater plate, the covering is not required because the surface of the resistance heating element is never oxidized. When a resistance heating element is formed inside a heater plate, a part of the surface of the resistance heating element may be exposed.

[0089] A desirable metal foil to be used as a resistance heating element is those formed as resistance heating elements by pattern formation through, for example, etching a nickel foil and a stainless steel foil. The patterned metal foil may also be put together with a resin film and the like. Examples of the metal wire include a tungsten wire and a molybdenum wire.

[0090] When a conductor is formed on the surface and inside the ceramic substrate of the present invention and the above internal conductor is at least one of a guard electrode or a ground electrode, the above-mentioned ceramic substrate functions as a wafer prober.

[0091] Fig. 6 is a sectional view schematically showing an embodiment of the wafer prober of the present invention. Fig. 7 is a sectional view of the wafer prober illustrated in Fig. 6 taken along the A-A line.

[0092] In this wafer prober 201, grooves 47 in a concentric configuration as viewed from the above are formed in the surface of a ceramic substrate 43 in a circular form as viewed from the above and a plurality of suction holes 48 for sucking a silicon wafer are provided in apart of the grooves 47. A chuck top conductor layer 42 for connecting to an electrode of a silicon wafer is formed, in a circular form, in a greater part of the ceramic substrate 43 including the grooves 47.

[0093] On the other hand, on the bottom face of the ceramic substrate 43, a heating elements 49 in a concentric configuration as viewed from the above as shown in Fig. 3 are disposed in order to control the temperature of a silicon wafer. To both ends of each of the heating elements 49, external terminal pins (not shown) are connected and fixed.

[0094] Moreover, a guard electrode 45 and a ground electrode 46 (reference to Fig. 7), both being in a lattice form as viewed from the above, are arranged inside the ceramic substrate 43 in order to remove a stray capacitor and noise. The materials of the guard electrodes 45 and ground electrodes 46 may be the same as those of the electrostatic electrodes.

[0095] The thickness of the above-mentioned chuck top conductor layer 42 is desirably 1 to 20 μm because if it is less than 1 μm , the resistivity is too high for the layer to work as an electrode and, on the other hand, if it exceeds 20 μm , the layer becomes easier to exfoliate due to the stress that the conductor has.

[0096] As the chuck top conductor layer 42, at least one kind of metal selected from high-melting point metals such as copper, titanium, chromium, nickel, noble metals (gold, silver, platinum and the like), tungsten and molybdenum can be employed.

[0097] As for a wafer prober having such a structure, a continuity test can be performed by placing, on the wafer prober, a silicon wafer having an integrated circuit formed thereon, pressing a probe card having a tester pin against the silicon wafer and applying a voltage while heating and cooling.

[0098] Next, a manufacturing method of the ceramic substrate of the present invention will be explained with reference to the sectional views shown in Figs. 8(a) to (d) by taking a manufacturing method of an electrostatic chuck as an example.

(1) First, a green sheet 50 is made by preparing a mixed composition by mixing a powder of ceramic such as oxide ceramic, nitride ceramic and carbide ceramic with a binder and a solvent and thereafter shaping the composition. When incorporating carbon into the composition, the above-mentioned crystalline carbon or amorphous carbon is used and the amount thereof is controlled depending upon the desired characteristics.

As the ceramic powder, for example, aluminum nitride, silicon carbide and the like can be used. A sintering aid such as yttria may be added based on the necessity.

When an amorphous carbon is used as carbon, it is desirable to produce the amorphous carbon in advance, but it is also possible that a material that will change to amorphous carbon is mixed in a green sheet. For example, pure amorphous carbon can be produced by firing a hydrocarbon made up only of C, H and O, preferably a saccharide (sucrose or cellulose), in the air at 300 to 500 $^{\circ}\text{C}$. As a crystalline carbon, pulverized crystalline carbon

black or graphite, which are crystalline, can be employed.

Several or one green sheet 50 which will be laminated on a green sheet on which the electrostatic electrode printed element 51 has been formed, described later, is a layer that will become a ceramic dielectric film. The composition thereof, therefore, may be a composition different from that of the ceramic substrate, depending upon the purpose and the like.

Moreover, it is also possible that a ceramic substrate is manufactured first, an electrostatic electrode layer is thereafter formed thereon, and a ceramic dielectric film is formed further thereon.

As a binder, at least one selected from acrylic binders, ethylcellulose, butyl cellosolve and polyvinyl alcohol is desirable.

Furthermore, as a solvent, at least one selected from α -terpineol and glycol is desirable.

A green sheet 50 is made by shaping the paste obtained by mixing these materials into a sheet form by a doctor blade method.

To the green sheet 50, a through hole in which a lifter pin for a silicon wafer will be inserted or a concave portion in which a thermocouple will be buried may also be formed based on the necessity. The through hole or the concave portion may be formed by punching and the like.

The thickness of the green sheet 50 is desirably about 0.1 to 5 mm.

(2) Subsequently, a conductor containing paste that will become an electrostatic electrode layer or a resistance heating element is printed to the green sheet 50.

The printing is performed in order that a desired aspect ratio is obtained, by considering a shrinkage ratio of the green sheet 50. By this operation, an electrostatic electrode layer printed element 51 and a resistance heating element layer printed element 52 are obtained.

The printed elements are formed by printing a conductor containing paste containing a conductive ceramic, metal particles and the like.

As the conductive ceramic particles contained in these conductor containing pastes, a carbide of tungsten or molybdenum is the most suitable since they are hard to be oxidized and their conductivity does not drop easily.

As the metal particles, for example, tungsten, molybdenum, platinum and nickel can be employed.

The conductive ceramic particles and the metal particles desirably have an average particle diameter of 0.1 to 5 μm because if these particles are too large or too small, it is difficult to print the conductor containing paste.

As such a paste, a conductor containing paste which is prepared by mixing 85 to 97 parts by weight of metal particles or conductive ceramic particles, 1.5 to 10 parts by weight of at least one kind of binder selected from acrylic binders, ethylcellulose, butyl cellosolve and polyvinyl alcohol and 1.5 to 10 parts by weight of at least one kind of solvent selected from α -terpineol, glycol, ethylalcohol and butanol is most suitable.

Furthermore, conductor-filled through hole printed elements 53, 54 are obtained by filling the holes formed by punching and the like with the conductor containing paste.

(3) Next, as illustrated in Fig 8(a), a green sheet 50 with printed elements 51, 52, 53, 54 and a green sheet 50 with no printed element are laminated. On the green sheet on which an electrostatic electrode printed element 51 has been formed, several or one green sheet 50 is laminated. The purpose of laminating a green sheet 50 having no printed element on the face where a resistance heating element is formed is to prevent the trouble that an end surface of a conductor-filled through hole is exposed and oxidized during the sintering for forming a resistance heating element. If the sintering for forming a resistance heating element is performed with the end surface of the conductor-filled through hole exposed, there is a necessity of sputtering with metal which are hard to be oxidized, such as nickel. More desirably, it may also be covered with an Au-Ni gold solder.

(4) Next, as illustrated in Fig. 8(b), the green sheet and the conductor containing paste are sintered by heating and pressurizing the lamination. The heating temperature is desirably 1000 to 2000 $^{\circ}\text{C}$ and the pressure is desirably 100 to 200 kg/cm^2 . The heating and pressurizing are performed in an inert gas atmosphere. As the inert gas, argon, nitrogen and the like can be employed. In this step, conductor-filled through holes 16, 17, a chuck positive electrostatic layer 2, a chuck negative electrostatic layer 3, a resistance heating element 5 and so on, are formed.

(5) Next, as illustrated in Fig. 8(c), blind holes 13, 14 for connecting external terminals are formed.

It is favorable that at least a part of the inner walls of the blind holes 13, 14 is made conductive and the inner walls which have been made conductive are connected with a chuck positive electrostatic layer 2, chuck negative electrostatic layer 3, resistance heating element 5, and so on.

(6) Finally, as illustrated in Fig. 8(d), external terminals 6, 18 are attached to the blind holes 13, 14 through a gold solder. Moreover, based on the necessity, a bottomed hole 12 may be formed, into which a thermocouple is buried.

[0099] Alloy such as silver-lead, lead-tin and bismuth-tin can be used as a solder.

[0100] The thickness of the solder layer is desirably 0.1 to 50 μm because this is the range sufficient to keep a connection with a solder.

[0101] An electrostatic chuck 101 (reference to Fig.1) was taken as an example in the above explanation. When

manufacturing a wafer prober, a ceramic substrate with a resistance heating element embedded therein can be manufactured first, as in the case of electrostatic chucks, thereafter a groove in the surface of the ceramic substrate is formed thereon, and then a metal layer can be formed by conducting sputtering, plating and the like to the surface portion where the groove has been formed.

Best Mode for Carrying out the Invention

[0102] The present invention will be described in more detail below.

(Examples 1 to 3) Hot plate (reference to Fig. 9)

[0103]

(1) An aluminum nitride sintered body was obtained by: mixing 100 parts by weight of aluminum nitride powder (manufactured by Tokuyama Corp., average particle diameter 1.1 μm) which is fired in the air at 500 °C for one hour, 4 parts by weight of yttrium oxide (Y_2O_3 : yttria, average particle diameter 0.4 μm) and 11.5 parts by weight of acrylic resin binder; putting the mixture into a hexagonal columnar mold; and then hot pressing it in a nitrogen atmosphere under the conditions: at a temperature of 1890 °C and a pressure of 150 kg/cm² for three hours. Ceramic substrates having, respectively, a diameter of 280 mm and a thickness of 19 mm (Example 1), a diameter of 310 mm and a thickness of 5 mm (Example 2) and a diameter of 350 mm and a thickness of 3mm (Example 3) were obtained by processing the above aluminum nitride sintered body into disk shapes and varying a surface grinding amount.

(2) A conductor containing paste was printed on the bottom face 91a of the ceramic substrate 91 obtained in the above (1) by screen printing. The printed pattern was that in a concentric configuration shown in Fig. 3.

As the conductor containing paste, Solbest PS603D manufactured by Tokuriki Kagaku Kenkyusho.CO, which has been used for the formation of plated through holes in printed circuit boards, was used.

This conductor containing paste was a silver-lead paste and contained 7.5 parts by weight, per 100 parts by weight of silver, of metal oxides comprising lead oxide (5% by weight), zinc oxide (55% by weight), silica (10% by weight), boron oxide (25% by weight) and alumina (5% by weight). The silver powder had an average particle diameter of 4.5 μm and was in a scaly shape.

(3) Next, a resistance heating element 92 was formed by heating and firing the sintered body printed with the conductor containing paste at 780 °C, thereby sinter the silver and the lead in the conductor containing paste and baking them to the ceramic substrate 91. The silver-lead resistance heating element 92 had a thickness of 5 μm , a width of 2.4 mm and a volume resistivity of 7.7 m Ω/\square .

(4) A metal covering layer 92a (a nickel layer) 1 μm in thickness was deposited on the surface of the silver-lead heating element 92 by immersing the sintered body prepared in the above (4) in an electroless nickel plating bath comprising an aqueous solution containing 80 g/L of nickel sulfate, 24 g/L of sodium hypophosphite, 12 g/L of sodium acetate, 8 g/L of boric acid and 6 g/L of ammonium chloride.

(5) A solder layer was formed by printing a silver-lead solder paste (manufactured by Tanaka Kikinzoku Kogyo CO.) by screen printing in a portion to which a terminal for securing the connection with a power supply will be attached. Subsequently, by placing a terminal pin 93 made of Kovar on the solder layer and heating and reflowing at 420 °C, the terminal pin 93 was attached to a surface of the heating element 92 (the metal covering layer 92a).

(6) Then, a ceramic heater 90 (reference to Fig. 9) was obtained by inserting a thermocouple for temperature control in a bottomed hole 94, filling with a polyimide resin and curing it at 190 °C for 2 hours.

(Test examples 1 to 3)

[0104] Hot plates were manufactured in the same manner as Example 1 except that they are adjusted to have a diameter of 240 mm and a thickness of 5 mm (Test example 1); a diameter of 310 mm and a thickness of 30 mm (Test example 2); and a diameter of 300 mm and a thickness of 17 mm without adding any Y_2O_3 (Test example 3). In Test example 3, a metal foil serving as a resistance heating element was embedded in the mold and a formed position of the heating element was adjusted to the position 33% apart from the back face.

(Test examples 4 to 6)

[0105] Hot plates were manufactured in the same manner as Example 1 except that they are provided with no heating element and adjusted to have a diameter of 240 mm and a thickness of 5 mm (Test example 4); a diameter of 310 mm and a thickness of 30 mm (Test example 5); and a diameter of 300 mm and a thickness of 17 mm (Test example 6).

[0106] As for the hot plates according to the above-mentioned Examples 1 to 3 and Test examples 1 to 6, a warp amount at 450 °C, a temperature rise time, temperature uniformity of a surface and an oxygen amount were examined in the manner described below. It is to be noted that for the hot plates of Test examples 4 to 6, only the warp amount was examined. The results are shown in the table.1.

(Examples 4 to 6) Alumina hot plate

[0107]

(1) Green sheets 50, which were 0.47 mm in thickness, were obtained: by using a paste containing alumina:93% by weight, SiO₂:5% by weight, CaO:0.5% by weight, MgO:0.5% by weight, TiO₂:0.5% by weight, an acrylic binder: 11.5 parts by weight, a dispersant: 0.5 part by weight and 53 parts by weight of alcohol comprising 1-butanol and ethanol; and forming it by a doctor blade method.

(2) Subsequently, after drying these green sheets at 80 °C for 5 hours, to the green sheets having a necessity of being processed, portions which will become through holes, in which lifter pins for a semiconductor wafer, 1.8 mm, 3.0 mm and 5.0 mm in diameter, respectively, and portions which will become conductor-filled through holes for being connected with external terminals were formed by punching.

(3) Conductor containing paste B was prepared by mixing 100 parts by weight of tungsten particle having an average particle diameter of 3 µm, 1.9 parts by weight of an acrylic binder, 3.7 parts by weight of α-terpineol solvent and 0.2 part by weight of a dispersant.

A conductor containing paste layer was formed by printing this conductor containing paste B to the green sheets 50 by screen printing. The printed pattern was a pattern in a concentric configuration.

(4) Furthermore, conductor containing paste B was filled in the through hole for conductor-filled through holes for being connected with external terminals.

On the green sheets 50 with a pattern of a resistance heating element formed thereon, 34 to 60 sheets of green sheets 50 with no conductor containing paste printed were stacked on their top side (the heating face); and the same 13 to 30 green sheets were stacked on their bottom side. Then, the resultant was pressed at a temperature of 130 °C and a pressure of 80 kg/cm² to form a lamination (Fig. 8(a)). The heating element-formed positions are shown in Table 2.

(5) Next, alumina plates of 3 mm in thickness were obtained by degreasing the obtained lamination at 600 °C for 5 hours and hot pressing them at 1600 °C at a pressure of 150 kg/cm² for 2 hours. By varying the processing conditions and grinding conditions, ceramic substrates made of alumina having a diameter of 280 mm and a thickness of 19 mm (Example 4), a diameter of 310 mm and a thickness of 5 mm (Example 5), and a diameter of 350 mm and a thickness of 3 mm (Example 6), respectively, were obtained. These ceramic substrates had resistance heating elements 5 having a thickness of 6 µm and a width of 10 mm therein.

(6) Subsequently, after the grinding of the plates obtained in (5) with a diamond grind stone, masks were placed on the bodies, and bottomed holes (diameter:1.2 mm, depth:2.0 mm) for a thermocouple were formed on their surfaces by blast treatment with SiC and the like.

(7) Furthermore, blind holes 13, 14 were formed by hollowing out the portions where the conductor-filled through holes were made (Fig 8(c)), and external terminals 6, 18 made of Kovar were connected to the blind holes 13, 14 by heating and reflowing using gold solder comprising Ni-Au (Fig 8(d)) at 700 °C. It is to be noted that the connection of the external terminals desirably has a structure wherein a support of tungsten supports at three points since connection reliability can be secured.

(8) Next, a plurality of thermocouples for temperature control were buried in the bottomed holes. Thus, the manufacturing of the hot plates with resistance heating elements was completed.

(Test examples 7 to 9)

[0108] Ceramic substrates were manufactured in the same manner as Examples 4 to 6 except that they are adjusted to have a diameter of 240 mm and a thickness of 5 mm (Test example 7); a diameter of 310 mm and a thickness of 30 mm (Test example 8); and a diameter of 300 mm and a thickness of 17 mm using aluminum nitride without containing any Y₂O₃ (Test example 9). The heating element-formed positions are shown in Table 2.

(Test examples 10 to 12)

[0109] Hot plates were manufactured in the same manner as Examples 4 to 6 except they are provided with no heating element and adjusted to a diameter of 240 mm and a thickness of 5 mm (Test example 10); a diameter of 310 mm and a thickness of 30 mm (Test example 11); and a diameter of 300 mm and a thickness of 17 mm (Test example 12).

(Test example 13)

[0110] A hot plate was manufactured in the same manner as Example 5 except laminating 20 sheets of green sheets 50 on the top side (heating face) and 19 sheets on the bottom side.

(Test example 14)

[0111] A hot plate was manufactured in the same manner as Example 5 except laminating 10 sheets of green sheets 50 on the top side (the heating face) and 29 sheets on the bottom side.

[0112] As for the hot plates of the above-mentioned Examples 4 to 6 and Test examples 7 to 14, a warp amount at 450°C, a temperature rise time and temperature uniformity of a surface were examined in the manner described below. It is to be noted that for the hot plates of Test examples 10 to 12, only the warp amount was examined. The results are shown in Table 2.

(Examples 7 to 9) Manufacture of electrostatic chucks made of AlN with heaters (Figs. 1 to 3)

[0113]

(1) Next, green sheets 50, which were 0.47 mm in thickness, were obtained by: using a paste prepared by mixing 100 parts by weight of aluminum nitride powder (manufactured by Tokuyama Corp., average particle diameter 1.1 μm) fired in the air at 500 °C for one hour, 1, 2, 4 parts by weight of yttria (average particle diameter 0.4 μm), 11.5 parts by weight of acrylic binder, 0.5 part by weight of a dispersant and 53 parts by weight of alcohol comprising 1-butanol and ethanol; and forming it by a doctor blade method.

(2) Subsequently, after drying these green sheets 50 at 80 °C for 5 hours, to the green sheets having a necessity of being processed, portions which will become through holes, in which lifter pins for a semiconductor, 1.8 mm, 3.0 mm and 5.0 mm in diameter, respectively, and portions which will become conductor-filled through holes for being connected with external terminals were formed by punching.

(3) Conductor containing paste A was prepared by mixing 100 parts by weight of tungsten carbide particles having an average particle diameter of 1 μm , 3.0 parts by weight of an acrylic binder, 3.5 parts by weight of α -terpineol solvent and 0.3 part by weight of a dispersant.

Conductor containing paste B was prepared by mixing 100 parts by weight of tungsten particles having an average particle diameter of 3 μm , 1.9 parts by weight of an acrylic binder, 3.7 parts by weight of α -terpineol solvent and 0.2 part by weight of a dispersant.

A conductor containing paste layer was formed by printing this conductor containing paste A to the green sheets 50 by screen printing. The printed pattern was a pattern in a concentric configuration. Moreover, on another green sheet 50, a conductor containing paste layer having an electrostatic electrode pattern with a shape shown in Fig. 2 was formed.

(4) Furthermore, conductor containing paste B was filled in the through hole for conductor-filled through holes for being connected with external terminals.

On the green sheets 50 with a pattern of a resistance heating element formed thereon, 34 to 60 sheets of green sheets 50 with no conductor containing paste printed were stacked on their top side (the heating face); and the same 13 to 30 green sheets were stacked on their bottom side. Then, the resultant was pressed at a temperature of 130 °C and a pressure of 80 kg/cm² to form a lamination (Fig. 8(a)). The heating element-formed positions are shown in Table 2.

(5) Next, the resulting laminations were processed to substrates, respectively, having a diameter of 280 mm and a thickness of 19 mm (Example 7), a diameter of 310 mm and a thickness of 5 mm (Example 8) and a diameter of 350 mm and a thickness of 3 mm (Example 9) under nitrogen gas by degreasing the laminations at 600 °C for 5 hours, hot pressing them at 1890 °C at a pressure of 150 kg/cm² for 3 hours and varying the processing conditions and grinding conditions. Thus, plates made of aluminum nitride were obtained, which had therein resistance heating elements 5 having a thickness of 6 μm and a width of 10 mm and a chuck positive electrostatic layer 2 and a chuck negative electrostatic layer 3, each layer being 10 μm in thickness (Fig. 8(b)). The heating element-formed positions are shown in Table 3.

(6) Subsequently, after the grinding of the plate obtained in (5) with a diamond grind stone, a mask was placed on the body and a bottomed hole (diameter: 1.2 mm, depth: 2.0 mm) for a thermocouple was formed in its surface by blast treatment with SiC and the like.

(7) Furthermore, blind holes 13, 14 were formed by hollowing out the portions where the conductor-filled through holes were made (Fig 8(c)) and external terminals 6, 18 made of Kovar were connected to the blind holes 13, 14 by heating and reflowing using gold solder comprising Ni-Au (Fig 8(d)) at 700 °C. It is to be noted that the connection

of the external terminals desirably has a structure, wherein a support of tungsten supports at three points, since connection reliability can be secured.

(8) Next, a plurality of thermocouples for temperature control were buried in the bottomed holes. Thus, the manufacture of the electrostatic chucks with resistance heating elements was completed.

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(Test examples 15 to 17)

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[0114] Ceramic substrates were manufactured in the same manner as Examples 7 to 9 except that they are adjusted to have a diameter of 240 mm and a thickness to 5 mm (Test example 15) ; a diameter of 310 mm and a thickness of 30 mm (Test example 16) ; and a diameter of 300 mm and a thickness of 17 mm without adding any Y_2O_3 (Test example 17).

(Test examples 18 to 20)

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[0115] Ceramic substrates were manufactured in the same manner as Examples 7 to 9 except that they are provided with no heating element and adjusted to have a diameter of 240 mm and a thickness of 5 mm (Test example 18) ; a diameter of 310 mm and a thickness of 30 mm (Test example 19) ; and a diameter of 300 mm and a thickness of 17 mm (Test example 20).

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(Test example 21)

[0116] Ceramic substrates were manufactured in the same manner as in Examples 7 to 9 except laminating 20 sheets of green sheets 50 on the top side (the heating face) and 19 sheets on the bottom side.

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(Test example 22)

[0117] Ceramic substrates were manufactured in the same manner as Examples 7 to 9 except laminating 10 sheets of green sheets 50 on the top side (the heating face) and 29 sheets on the bottom side.

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(Test example 23)

[0118] A ceramic substrate was manufactured in the same manner as Example 8 except adding no yttria to aluminum nitride.

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(Test example 24)

[0119] A ceramic substrate was manufactured in the same manner as Example 8 except adding 40 parts by weight of yttria to aluminum nitride.

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[0120] As for the electrostatic chucks of the above-mentioned Examples 7 to 9 and Test examples 15 to 24, a warp amount at 450 °C, a temperature rise time, temperature uniformity of a surface and an oxygen amount were examined in the manner described below. It is to be noted that for the electrostatic chucks of Test examples 18 to 20, only the warp amount was examined. The results are shown in Table 3.

(Example 10) Manufacture of electrostatic chuck

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[0121]

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(1) A green sheet, which was 0.47 mm in thickness, was obtained by : using a paste prepared by mixing 100 parts by weight of aluminum nitride powder fired in the air at 500°C for one hour (manufactured by Tokuyama Corp., average particle diameter 1.1 μm), 4 parts by weight of yttria (average particle diameter 0.4 μm), 11.5 parts by weight of acrylic binder, 0.5 part by weight of a dispersant and 53 parts by weight of alcohol comprising 1-butanol and ethanol; and forming it by a doctor blade method.

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(2) Subsequently, after drying these green sheets at 80 °C for 5 hours, to the green sheets having a necessity of being processed, portions which will become through holes, in which lifter pins for a semiconductor wafer, 1.8 mm, 3.0 mm and 5.0 mm in diameter, respectively, and portions which will become conductor-filled through holes for being connected with external terminals were formed by punching.

(3) Conductor containing paste A was prepared by mixing 100 parts by weight of tungsten carbide particles having an average particle diameter of 1 μm , 3.0 parts by weight of an acrylic binder, 3.5 parts by weight of α -terpineol

and 0.3 part by weight of a dispersant.

Conductor containing paste B was prepared by mixing 100 parts by weight of tungsten particles having an average particle diameter of 3 μm , 1.9 parts by weight of an acrylic binder, 3.7 parts by weight of α -terpineol and 0.2 part by weight of a dispersant.

By printing this conductor containing paste A to the green sheet by screen printing, a conductor containing paste layer having an electrostatic electrode pattern with a shape shown in Fig. 5 was formed.

Furthermore, conductor containing paste B was filled in the through hole for conductor-filled through holes for being connected with external terminals.

On the green sheet with the electrostatic electrode pattern formed thereon, 1 green sheet with no tungsten paste printed was stacked on its top side (the heating face); and 48 green sheets were stacked on its bottom side. Then, the resultant was pressed at a temperature of 130 °C and a pressure of 80 kg/cm² to form a lamination (Fig. 8(a)).

(4) Next, an aluminum nitride plate 3 mm in thickness was obtained by degreasing the obtained lamination in the nitrogen gas at 600 °C for 5 hours and hot pressing it at 1890 °C at a pressure of 150 kg/cm² for 3 hours. This lamination was cut into a disk-shape of 300 mm in diameter to form a plate made of aluminum nitride having therein a chuck positive electrostatic layer 2 and a chuck negative electrostatic layer 3, each layer having a thickness of 10 μm .

(5) Subsequently, a mask was placed on the bottom face of the plate obtained in (4) and concave portions for thermocouples (not shown) and the like were formed on the surface by blast treatment with SiC and the like.

(6) Next, a resistance heating element was printed on the face opposite to the wafer treating face. For the printing, a conductor containing paste was used. The conductor containing paste used was Solbest PS603D manufactured by Tokuriki Kagaku Kenkyusyo:CO, which has been used for the formation of plated through holes in printed circuit boards. This conductor containing paste was a silver/lead paste and contained 7.5 parts by weight, per 100 parts by weight of silver, of metal oxides comprising lead oxide, zinc oxide, silica, boron oxide and alumina (the weight ratio thereof being 5/55/10/25/5).

The silver had a scaly shape having an average particle diameter of 4.5 μm .

(7) Next, the silver and the lead in the conductor containing paste were sintered and baked on the ceramic substrate by heating and firing, at 780 °C, the plate printed with the conductor containing paste. Subsequently, a nickel layer 1 μm in thickness containing 1% by weight or less of boron was deposited on the surface of the silver sintered body 15 by the immersion of the plate in an electroless nickel plating bath comprising an aqueous solution containing 30 g/L of nickel sulfate, 30 g/L of boric acid, 30 g/L of ammonium chloride and 60 g/L of Rochelle salt. The plate was, then, subjected to annealing treatment at 120 °C for 3 hours.

The resistance heating element comprising the silver sintered body was 5 μm in thickness and 2.4 mm in width and had an area resistivity of 7.7 m Ω/\square .

(8) Next, the ceramic substrate was provided with blind holes to expose a conductor-filled through hole 16. To this blind hole, an external terminal pin made of Kovar was connected by using a gold solder comprising Ni-Au alloy (Au 81.5% by weight, Ni 18.4% by weight, impurities 0.1% by weight) and heating and reflowing at 970 °C. Moreover, an external terminal pin made of Kovar was formed on the resistance heating element through a solder (tin 9/lead 1).

(9) Subsequently, a plurality of thermocouples for temperature control were buried in the concave portions, thus an electrostatic chuck is manufactured. As for the resulting electrostatic chuck, a warp amount at 450°C, a temperature rise time, temperature uniformity of a surface and an oxygen amount were examined. The results are shown in Table 3.

(Example 11) Manufacture of wafer prober 201 (reference to Fig. 6)

[0122]

(1) A green sheet 0.47 mm in thickness was obtained by forming, by a doctor blade method, a mixed composition prepared by mixing 100 parts by weight of aluminum nitride powder fired in the air at 500 °C for one hour (manufactured by Tokuyama Corp., average particle diameter 1.1 μm), 4 parts by weight of yttria (average particle diameter 0.4 μm), 0.9 part by weight of the amorphous carbon obtained in Example 1 and 53 parts by weight of alcohol comprising 1-butanol and ethanol.

(2) Subsequently, after drying this green sheet at 80 °C for 5 hours, a through hole for a conductor-filled through hole for connecting a heating element and an external terminal pin were formed by punching.

(3) Conductor containing paste A was prepared by mixing 100 parts by weight of tungsten carbide particles having an average particle diameter of 1 μm , 3.0 parts by weight of an acrylic binder, 3.5 parts by weight of α -terpineol solvent and 0.3 part by weight of a dispersant.

Conductor containing paste B was prepared by mixing 100 parts by weight of tungsten particle having an average particle diameter of 3 μm , 1.9 parts by weight of an acrylic binder, 3.7 parts by weight of α -terpineol solvent and 0.2 part by weight of a dispersant.

Subsequently, to the green sheet were printed a printed element in a lattice-shape for a guard electrode and printed element in a lattice-shape for a ground electrode by screen printing using this conductor containing paste A. Conductor containing paste B was filled in the through hole for a conductor-filled through hole for being connected with a terminal pin.

Moreover, the green sheet with printed element and 50 green sheets with no printed element were stacked at 130 °C at a pressure of 80 kg/cm² to obtain a lamination.

(4) Next, an aluminum nitride plate of 3 mm in thickness was obtained by degreasing this lamination in the nitrogen gas at 600 °C for 5 hours and hot pressing it at 1890 °C at a pressure of 150 kg/cm² for 3 hours. The resulting plate was cut into a disk-shape of 300 mm in diameter to form a plate made of ceramic. The size of the conductor-filled through hole 16 was 0.2 mm in diameter and 0.2 mm in depth.

Furthermore, both guard electrode 45 and ground electrode 46 had a thickness of 10 μm . The position where the guard electrode 45 was formed was 1 mm apart from the wafer treating face and where the ground electrode 46 was formed was 1.2 mm apart from the wafer treating face. The length of one side of the areas 46a where no conductor was formed in the guard electrode 45 and the ground electrode 46 was 0.5 mm.

(5) After grinding of the plate obtained in (4) with a diamond grind stone, a mask was placed on the body and a concave portion for a thermocouple and a groove 47 for wafer adsorption (width: 0.5 mm, depth: 0.5 mm) were provided on the surface by blast treatment with SiC and the like.

(6) Furthermore, a layer for forming a heating element 49 was printed on the face opposing the wafer treating face. For the printing, a conductor containing paste was used. The conductor containing paste used was Solbest PS603D manufactured by Tokuriki Kagaku Kenkyusyo:CO, which has been used for the formation of plated through holes in printed circuit boards. This conductor containing paste was a silver/lead paste and contained 7.5 parts by weight, per 100 parts by weight of silver, of metal oxides comprising lead oxide, zinc oxide, silica, boron oxide and alumina (the weight ratio thereof being 5/55/10/25/5).

The silver had a scaly shape having an average particle diameter of 4.5 μm .

(7) Next, the silver and the lead in the conductor containing paste were sintered and baked on the ceramic substrate 43 by heating and firing, at 780 °C, the heater plate printed with the conductor containing paste. Subsequently, a nickel layer (not shown) 1 μm in thickness containing 1% by weight or less of boron was deposited on the surface of the silver sintered body 49 by the immersion of the heater plate in an electroless nickel plating bath comprising an aqueous solution containing 30 g/L of nickel sulfate, 30 g/L of boric acid, 30 g/L of ammonium chloride and 60 g/L of Rochelle salt. The heater plate was, then, subjected to annealing treatment at 120 °C for 3 hours. The heating element comprising the silver sintered body was 5 μm in thickness and 2.4 mm in width and had an area resistivity of 7.7 m Ω/\square .

(8) On the surface with a groove 47, a titanium layer, a molybdenum layer and a nickel layer were formed sequentially by a sputtering method. The equipment used for the sputtering was SV-4540 manufactured by ULVAC JAPAN LTD. The sputtering conditions were as follows, the pressure: 0.6 Pa, the temperature: 100 °C, and the electric power: 200 W. The sputtering time was adjusted in the range from 30 seconds to one minute for every metal. An image detected with a fluorescent X-ray analyzer showed that the thicknesses of the resulting films were 0.3 μm for the titanium layer, 2 μm for the molybdenum layer and 1 μm for the nickel layer.

(9) A nickel layer of 7 μm in thickness containing 1% by weight or less of boron was deposited on the surface of the metal layer formed by the sputtering, by the immersion of the ceramic board obtained in (8) in an electroless nickel plating bath comprising an aqueous solution containing 30 g/L of nickel sulfate, 30 g/L of boric acid, 30 g/L of ammonium chloride and 60 g/L of Rochelle salt. The resulting ceramic board was subjected to annealing treatment at 120 °C for 3 hours.

No electric current was applied on the surface of the heating element, so the surface was not covered with any electrolytic nickel plating.

Furthermore, a gold plating layer of 1 μm in thickness was deposited on the nickel plating layer by immersing the surface in an electroless gold plating solution containing 2 g/L of potassium gold cyanide, 75 g/L of ammonium chloride, 50 g/L of sodium citrate and 10 g/L of sodium hypophosphite under the condition of 93 °C for one minute.

(10) An air suction hole 48 extending from the groove 47 to the back face was formed by drill processing and further a blind hole (not shown) for exposing a conductor-filled through hole 16 was formed. To this blind hole, an external terminal pin made of Kovar was connected by using a gold solder comprising Ni-Au alloy (Au 81.5% by weight, Ni 18.4% by weight, impurities 0.1% by weight) with heating and reflowing at 970°C. Moreover, an external terminal pin made of Kovar was formed on the heating element through a solder (tin 90% by weight/lead 10% by weight).

(11) Subsequently, a plurality of thermocouples for temperature control were buried in the concave portions, thus,

a wafer prober 201 is obtained. As for the obtained wafer prober 201, a warp amount at 200 °C, a temperature rise time and an oxygen amount were examined. The results are shown in Table 3.

(Examples 12 to 14) Electrostatic chucks made of SiC with a heater

[0123] Green sheets 50, which were 0.47 mm in thickness, were obtained by: using a paste prepared by mixing 100 parts by weight of silicon carbide powder fired in the air at 500 °C for one hour (manufactured by Yakushima Electric Industries, Ltd., average particle diameter 1.1 μm), 4 parts by weight of carbon, 11.5 parts by weight of acrylic binder, 0.5 part by weight of a dispersant and 53 parts by weight of alcohol comprising 1-butanol and ethanol; and forming it by a doctor blade method. A glass paste was applied to: a green sheet on which a conductor layer was to be formed; and a green sheet which was to become the uppermost layer among the green sheets. Incidentally, regarding the green sheet on which a conductor layer was to be formed, the glass paste was applied to both sides of conductor containing paste, thereby establishing a sandwich state. Thereafter, the same processes as (2) to (8) in Examples 7 to 9 were performed to manufacture electrostatic chucks.

[0124] The diameter and the thickness of the resulting electrostatic chucks and the position of the conductor layer in the electrostatic chucks were: diameter: 280 mm, thickness: 19 mm, 45% from the back face (Example 12) ; diameter: 310 mm, thickness: 5 mm, 33% from the back face (Example 13) ; and diameter: 350 mm, thickness: 3 mm, 20% from the back face (Example 14).

(Example 15) SiC hot plate

[0125] A silicon carbide sintered body was obtained: by mixing 100 parts by weight of silicon carbide powder (manufactured by Yakushima Electric Industries, Ltd., average particle diameter 1.1 μm) fired in the air at 500°C for one hour, 4 parts by weight of carbon and 11.5 parts by weight of acrylic resin binder; putting the mixture into a hexagonal columnar mold; and then hot pressing it in a nitrogen atmosphere under the following conditions : 1890 °C and a pressure of 150 kg/cm² for 3 hours. Thereafter, the same processes as (2) to (6) in Example 1 were performed to manufacture a ceramic substrate. This sintered body was processed into a disk having a diameter of 300 mm and a thickness of 3 mm (Example 15). A glass paste was applied to a surface of the resulting disk and was fired in the air at 1000 °C to form an insulating layer. Thereafter, a conductor containing paste was printed on the insulating layer, resulting in a hot plate.

(Test examples 25 to 27)

[0126] Manufacture was performed in the same manner as Example 15 but under such conditions that the following particulars are achieved; diameter: 240 mm, thickness: 5 mm, 33% from the back face (Test example 25); diameter: 310 mm, thickness: 30 mm, 33% from the back face (Test example 26); and diameter: 240 mm, thickness: 17 mm, 33% from the back face and SiC is not fired (Test example 27).

[0127] As for Examples 12 to 15 and Test examples 25 to 27, a warp amount at 450 °C, a temperature rise time, temperature uniformity of a surface and an oxygen amount were examined. The results are shown in Table 4.

(Test example 28)

[0128] This example was the same as Test examples 6 and 14 except that no through hole for a lifter pin was formed. In the case where no through hole for a lifter pin was formed, the warp amount was as much as about 10 μm even when a hot plate was heated to 450°C.

[0129] The ceramic substrates of the preceding Examples and Test examples were evaluated by the following methods. Evaluation methods

(1) Uniformity of surface temperature

Using a thermoviewer (IR162012-0012, manufactured by Nippon Datum Co., Ltd.), temperatures at some points in the wafer treating face of a ceramic substrate were measured and the difference between the maximum temperature and the minimum temperature was obtained.

(2) Temperature-rising property

The time required for raising temperature to 450 °C was measured.

(3) Warp amount

By raising temperature to 450 °C and cooling to 25 °C, the warp amount was measured with a configuration meter (Nanoway, manufactured by Kyocera Corporation).

(4) Oxygen amount

A sample sintered under the same conditions as a sintered body of the Example was pulverized with a tungsten mortar and a 0.01-gram portion of the sample was taken, and the oxygen amount is measured with an oxygen/nitrogen determinator (Model TC-136, manufactured by LECO Corporation) under the following conditions: a sample heating temperature 2200 °C and a heating time 30 seconds.

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Table 1

Example No.	Diameter (mm)	Thickness (mm)	Heating element-formed position	Warp amount (μm)	Temperature rise time (sec)	Temperature uniformity ($^{\circ}\text{C}$)	Oxygen amount (wt%)
Example 1	280	19	Back face	5	160	5	1.6
Example 2	310	5	Back face	10	96	4	1.6
Example 3	350	3	Back face	10	90	4	1.6
Test example 1	240	5	Back face	10	90	4	1.6
Test example 2	310	30	Back face	5	1000	10	1.6
Test example 3	300	17	33% from back face	30	1000	10	<0.05
Test example 4	240	5	None	10			
Test example 5	310	30	None	10			
Test example 6	300	17	None	70			

Table 2

Example No.	Diameter (mm)	Thickness (mm)	Heating element-formed position	Warp amount (μm)	Temperature rise time (min)	Temperature uniformity (°C)
Example 4	280	19	45% from back face	10	30	15
Example 5	310	5	33% from back face	10	20	10
Example 6	350	3	20% from back face	10	20	10
Test example 7	240	5	33% from back face	10	20	10
Test example 8	310	30	33% from back face	5	50	20
Test example 9	300	17	33% from back face	30	--	--
Test example 10	240	5	None	10		
Test example 11	310	30	None	10		
Test example 12	300	17	None	70		
Test example 13	300	5	50% from back face	20	30	20
Test example 14	300	5	66% from back face	70	30	20

Table 3

Example No.	Diameter (mm)	Thickness (mm)	Heating element-formed position	Warp amount (μm)	Temperature rise time (sec)	Temperature uniformity (°C)	Oxygen amount (wt%)
Example 7	280	19	45% from back face	10	200	7	0.4
Example 8	310	5	33% from back face	10	96	4	0.8
Example 9	350	3	20% from back face	10	90	4	1.6
Example 10	300	3	Back face	10	90	3	1.6
Example 11	300	3	Back face	8	60	--	1.6
Test example 15	240	5	33% from back face	10	90	4	1.6
Test example 16	310	30	33% from back face	5	1000	10	1.6
Test example 17	300	17	33% from back face	30	1000	10	<0.05
Test example 18	240	5	None	10			
Test example 19	310	30	None	10			
Test example 20	300	17	None	80			
Test example 21	300	5	50% from back face	20	90	7	1.6
Test example 22	300	5	66% from back face	80	90	10	1.6
Test example 23	300	5	33% from back face	80	110	10	<0.05
Test example 24	300	5	33% from back face	50	110	10	16

Table 4

Example No.	Diameter (mm)	Thickness (mm)	Heating element-formed position	Warp amount (μm)	Temperature rise time (sec)	Temperature uniformity ($^{\circ}\text{C}$)	Oxygen amount (wt%)
Example 12	280	19	45% from back face	10	200	6	0.3
Example 13	310	5	33% from back face	10	96	3	0.3
Example 14	350	3	20% from back face	10	90	3	0.3
Example 15	300	3	Back face	10	90	2	0.3
Test example 25	240	5	33% from back face	10	90	3	0.3
Test example 26	310	30	33% from back face	5	1000	10	0.3
Test example 27	300	17	33% from back face	70	1000	10	<0.05

As is clear from the results shown in Tables 1 to 4, the ceramic substrates of the hot plates and the like according to the examples took short temperature rise times and superior in both the temperature-following property and the

temperature uniformity. Furthermore, the warp amount at a high temperature can be made small.

Industrial Applicability

[0130] As described above, the present invention can provide a substantially uniform temperature distribution for a wafer treating face since the above-mentioned ceramic substrate has a thickness of 25 mm or less. For example, when a semiconductor wafer and the like is placed, the damage thereon and the like caused by the non-uniformity of the temperature of the wafer treating face can be prevented.

Claims

1. A ceramic substrate for a semiconductor-producing/examining device having a conductor formed on a surface of said ceramic substrate or inside said ceramic substrate, wherein said substrate is containing oxygen and has a disc form, the diameter thereof exceeding 250 mm and a thickness thereof being 25 mm or less.
2. The ceramic substrate for a semiconductor-producing/examining device according to claim 1, wherein said ceramic is a nitride ceramic or an oxide ceramic.
3. The ceramic substrate for a semiconductor-producing/examining device according to claim 1, wherein said ceramic is a carbide ceramic.
4. The ceramic substrate for a semiconductor-producing/examining device according to any of claims 1 to 3, wherein said ceramic substrate is used in a temperature range of 100 to 700 °C.
5. The ceramic substrate for a semiconductor-producing/examining device according to any of claims 1 to 4, wherein said ceramic substrate has a plurality of through holes in which lifter pins for a semiconductor wafer are inserted.
6. The ceramic substrate for a semiconductor-producing/examining device according to any of claims 1 to 5, wherein said conductor is formed in a region up to the position of 60% in a thickness-direction from the face opposite to a wafer treating face of said ceramic substrate.
7. A ceramic substrate for a semiconductor-producing/examining device having a conductor formed on a surface of said ceramic substrate, wherein said substrate has a disc form, the diameter thereof exceeding 250 mm and a thickness thereof being 25 mm or less.
8. The ceramic substrate for a semiconductor-producing/examining device according to claim 7, wherein said ceramic is a nitride ceramic containing oxygen or an oxide ceramic.
9. The ceramic substrate for a semiconductor-producing/examining device according to claim 7, wherein said ceramic is a carbide ceramic containing oxygen.
10. The ceramic substrate for a semiconductor-producing/examining device according to any of claims 7 to 9, wherein a conductor is formed on the face opposite to a wafer treating face.
11. The ceramic substrate for a semiconductor-producing/examining device according to any of claims 7 to 10,

wherein said ceramic substrate is used in a temperature range of 100 to 700 °C.

12. The ceramic substrate for a
semiconductor-producing/examining device according to any of claims 7 to 11,
5 wherein said ceramic substrate has a plurality of through holes in which lifter pins for a semiconductor wafer
are inserted.

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Fig. 1

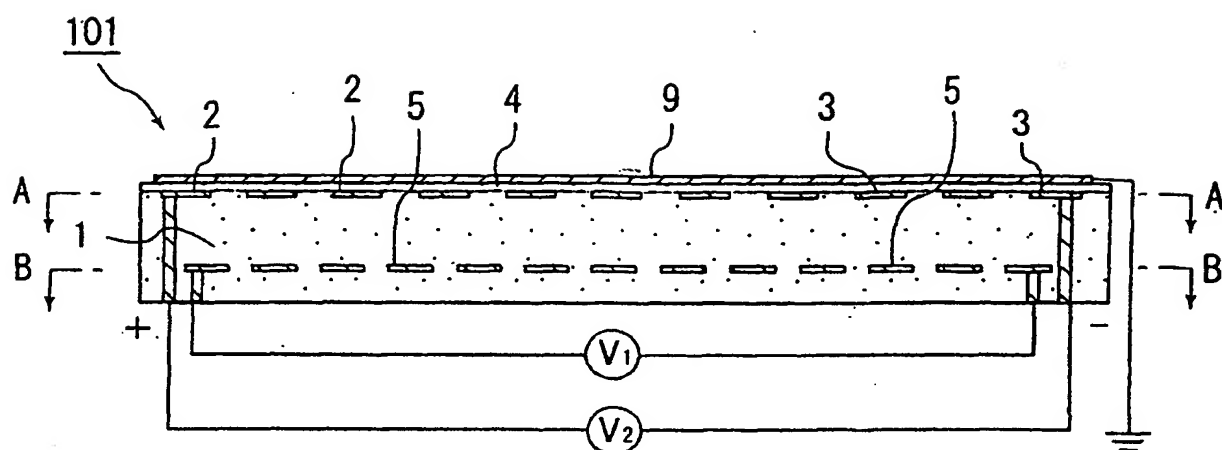


Fig. 2

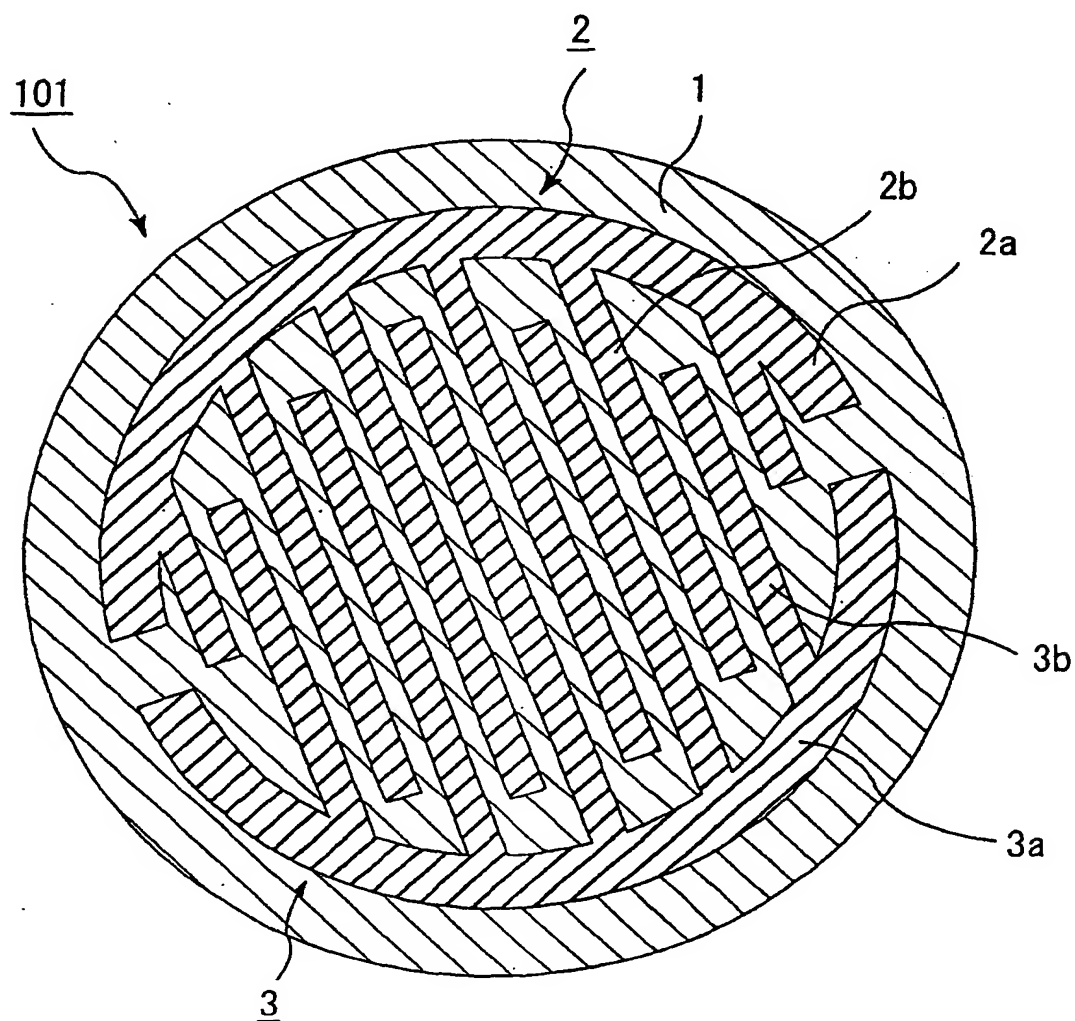


Fig. 3

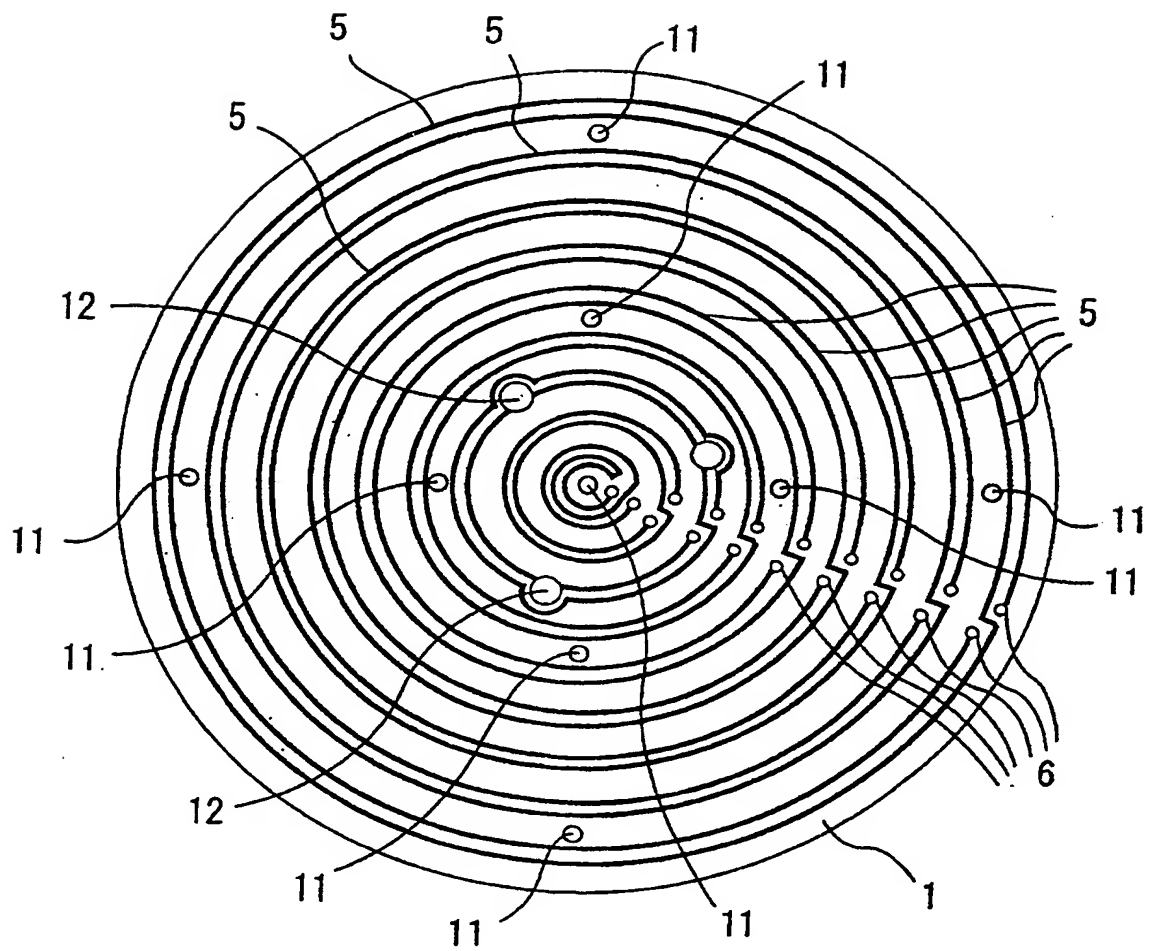


Fig. 4

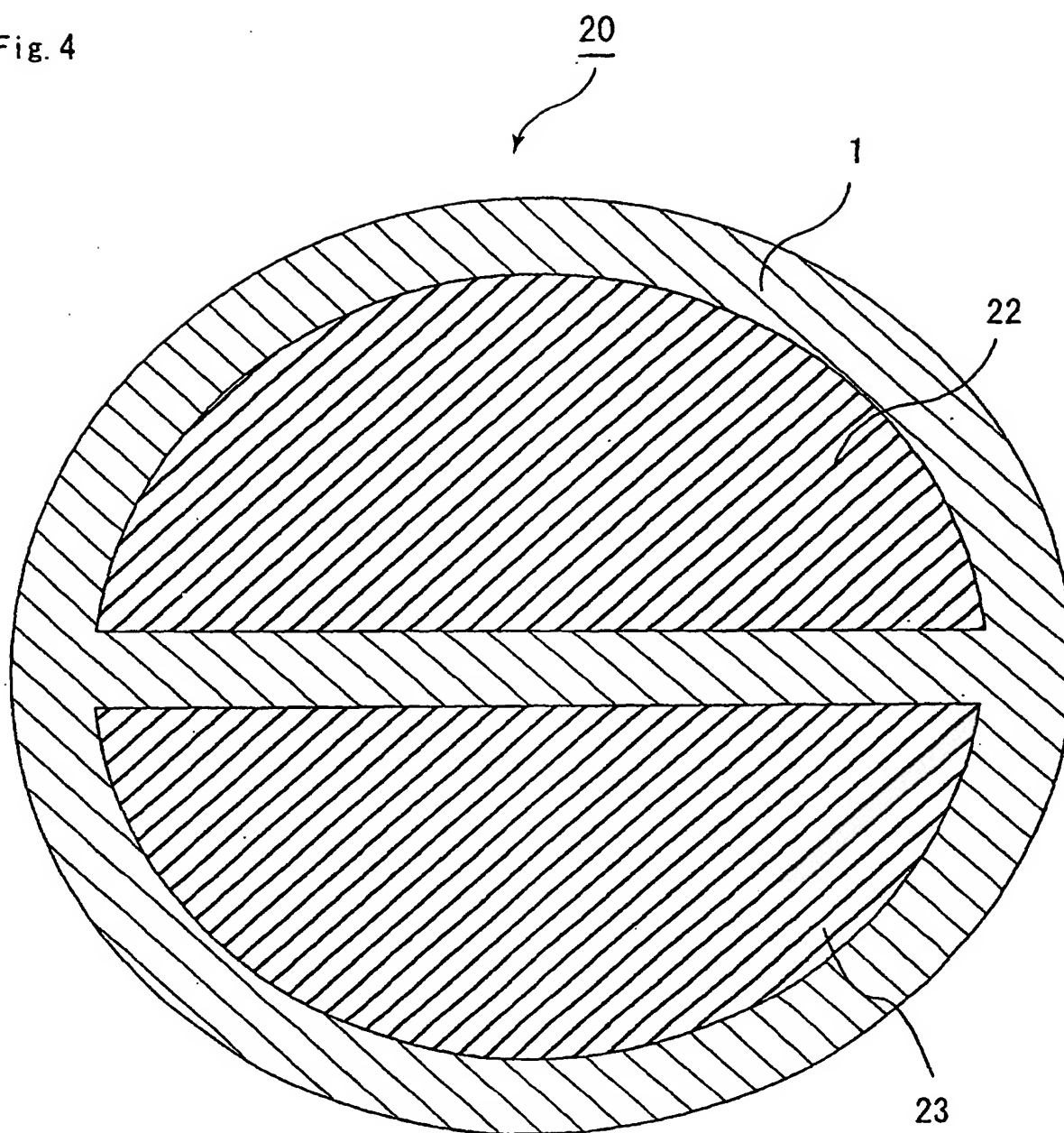


Fig. 5

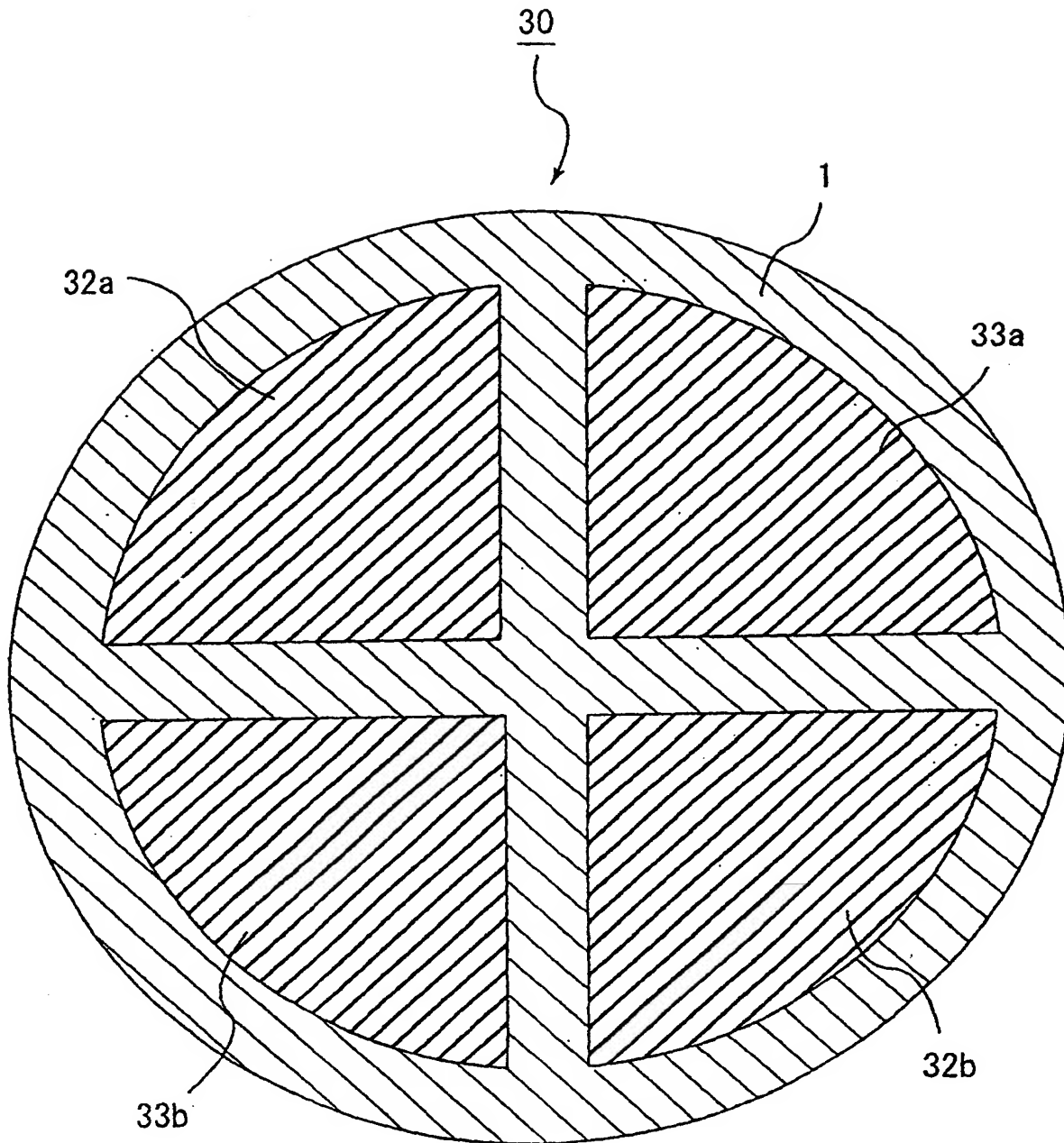


Fig. 6

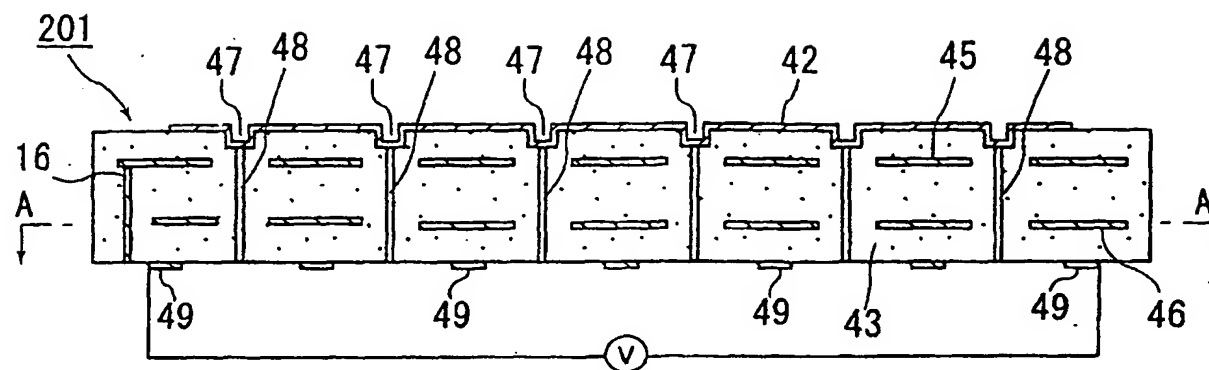


Fig. 7

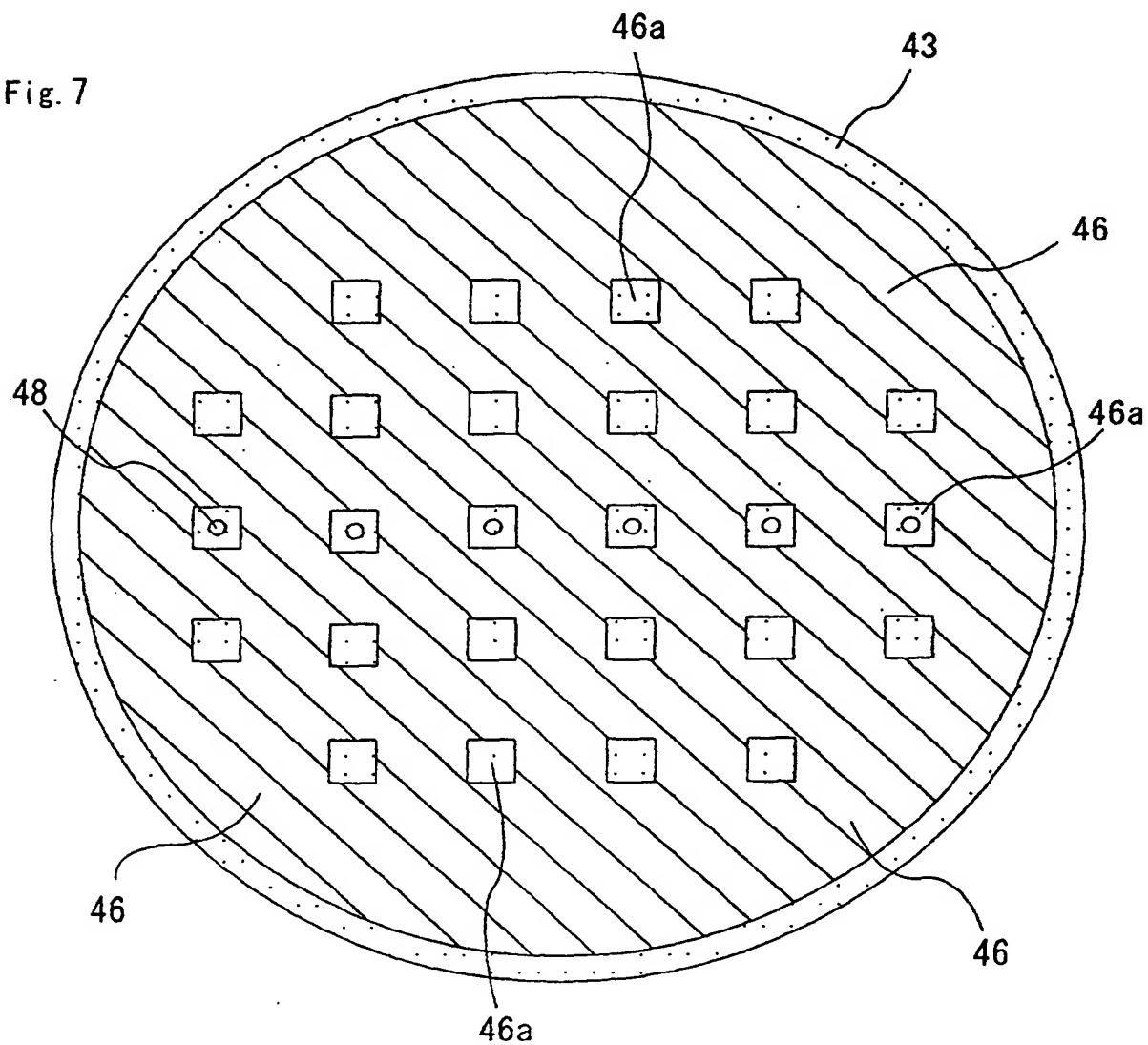


Fig. 8

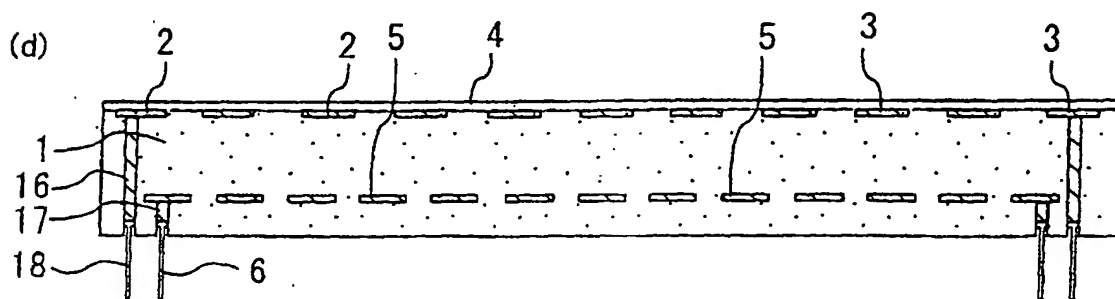
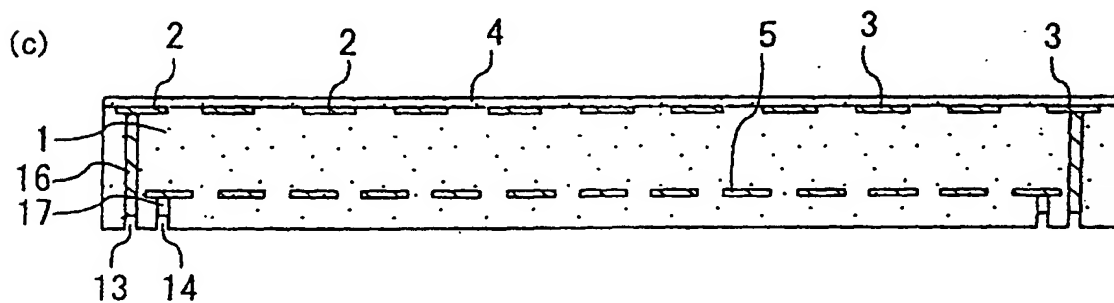
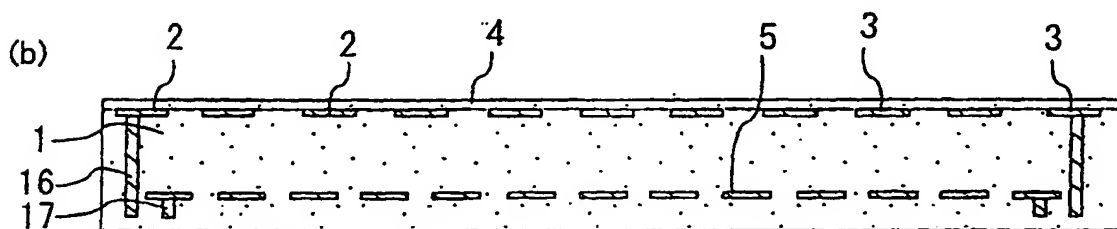
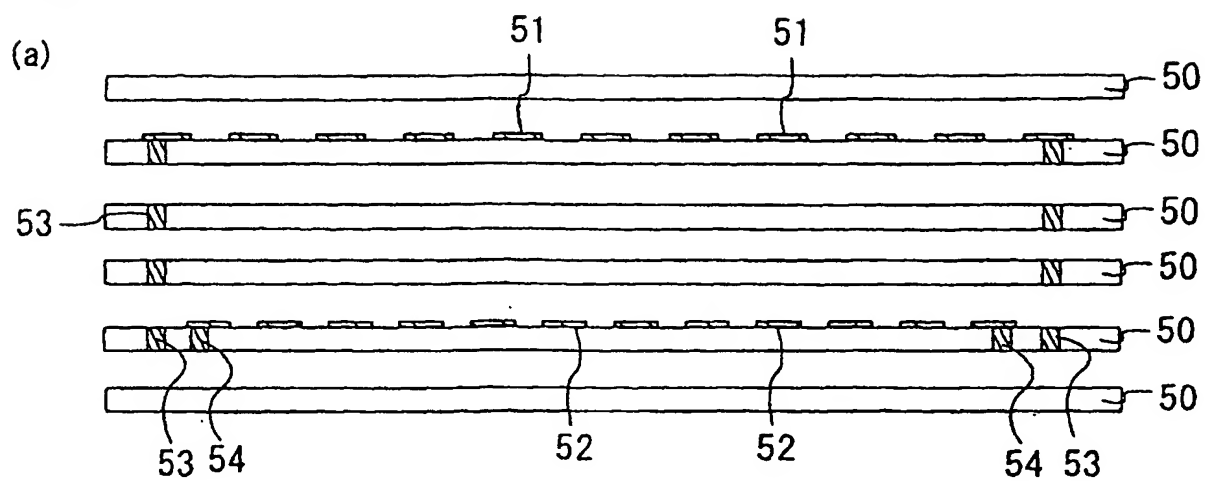
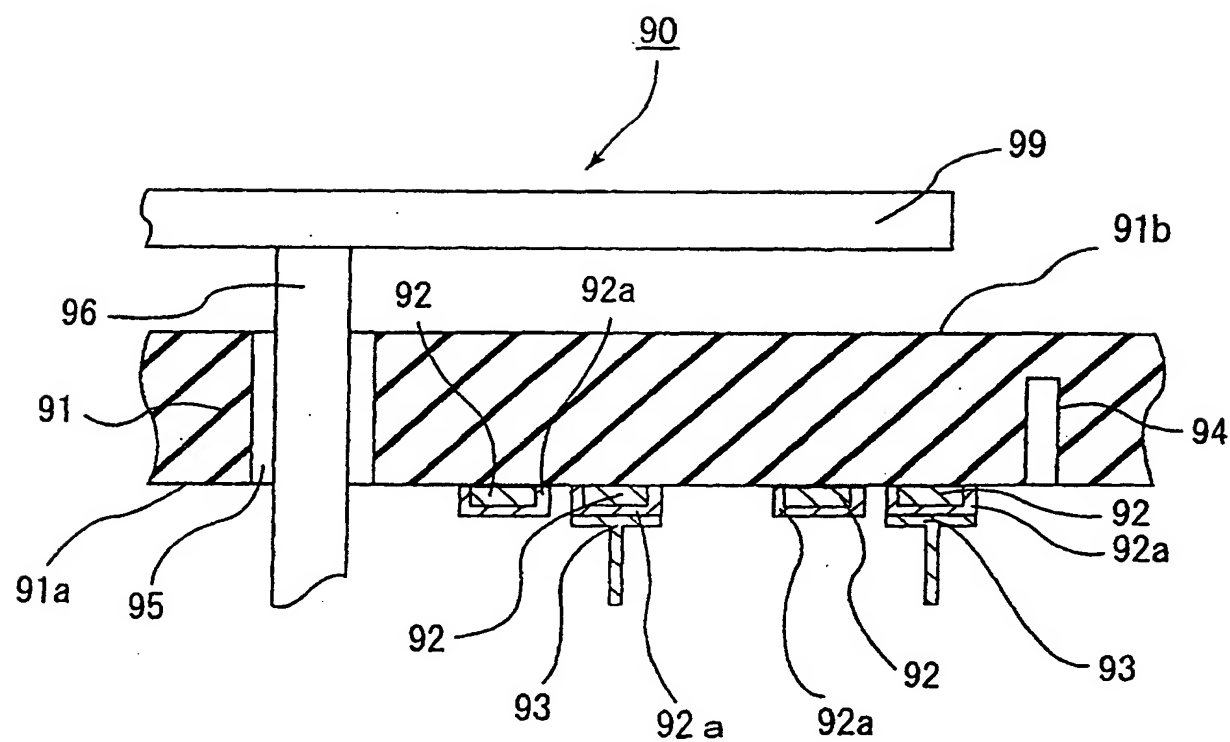


Fig. 9



INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP01/00889

A. CLASSIFICATION OF SUBJECT MATTER Int.Cl. ⁷ H01L21/68, H01L21/66, H05B3/10, H05B3/16, H05B3/18, H05B3/68, C04B35/00 According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) Int.Cl. ⁷ H01L21/68, H01L21/66, H05B3/10, H05B3/16, H05B3/18, H05B3/68, C04B35/00 Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Jitsuyo Shinan Koho 1926-1996 Toroku Jitsuyo Shinan Koho 1994-2001 Kokai Jitsuyo Shinan Koho 1971-2001 Jitsuyo Shinan Toroku Koho 1996-2001 Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	JP, 11-74064, A (Kyocera Corporation), 16 March, 1999 (16.03.99), Par. Nos. [0009] to [0012], [0016] to [0017], [0025], [0034] to [0039] (Family: none)	1, 2, 4, 6 3, 5, 7-12
Y	JP, 10-270540, A (Nippon Cement K.K.), 09 October, 1998 (09.10.98), Par. No. [0015] (Family: none)	3, 5
Y	JP, 10-308348, A (Tokyo Electron Limited), 17 November, 1998 (17.11.98), Par. Nos. [0004] to [0007] (Family: none)	5
Y	JP, 11-40330, A (IBIDEN CO., LTD.), 12 February, 1999 (12.02.99), Par. Nos. [0011] to [0013], [0022], [0029] to [0031] (Family: none)	7-12
Y	JP, 9-283607, A (Sumitomo Osaka Cement Co., Ltd.), 31 October, 1997 (31.10.97), abstract (Family: none)	3, 5, 9-12
<input checked="" type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> See patent family annex.		
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Date of the actual completion of the international search 28 May, 2001 (28.05.01)		Date of mailing of the international search report 05 June, 2001 (05.06.01)
Name and mailing address of the ISA/ Japanese Patent Office Facsimile No.		Authorized officer Telephone No.

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INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP01/00889

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	JP, 7-326655, A (Kyocera Corporation), 12 December, 1995 (12.12.95), Par. No. [0029] (Family: none)	1, 2, 4-6, 8, 10-12

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